Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ (please print)

Signature: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

ECE 2300 – Quiz #4

April 3, 2013

Keep this quiz closed and face up until you are told to begin.

1. This quiz is closed book, closed notes. You may use one 8.5” x 11” crib sheet, or its equivalent.

2. Show all work on these pages. Show all work necessary to complete the problem. A solution without the appropriate work shown will receive no credit. A solution which is not given in a reasonable order will lose credit.

3. It is assumed that your work will begin on the same page as the problem statement. If you choose to begin your work on another page, you must indicate this on the page with the problem statement, with a clear indication of where the work can be found. **If your work continues on to another page, indicate clearly where your work can be found. Failure to indicate this clearly will result in a loss of credit.**

4. Show all units in solutions, intermediate results, and figures. Units in the quiz will be included between square brackets.

5. Do not use red ink. Do not use red pencil.

6. You will have 30 minutes to work on this quiz.

\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/20

Room for extra work

The circuit below has an independent voltage source with a value of *vS1*.

1. Find the Thévenin equivalent of this circuit as seen by the independent voltage source, *vS1*.
2. Find the power absorbed by the independent voltage source, *vS1* in this circuit, when *vS1* = 72[V].
3. Find the power absorbed by the independent voltage source, *vS1* in this circuit, when *vS1* = -72[V].



Room for extra work

ECE 2300 -- Quiz #4 – April 3, 2013 – Solution

The circuit below has an independent voltage source with a value of *vS1*.

1. Find the Thévenin equivalent of this circuit as seen by the independent voltage source, *vS1*.
2. Find the power absorbed by the independent voltage source, *vS1* in this circuit, when
*vS1* = 72[V].
3. Find the power absorbed by the independent voltage source, *vS1* in this circuit, when
*vS1* = -72[V].

