Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ (please print)

Signature: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

ECE 2202 – Final Exam

May 3, 2018

Keep this exam closed until you are told to begin.

1. This exam is closed book, closed notes. You may use one 8.5” x 11” crib sheet, or its equivalent.

2. Show all work on these pages. Show all work necessary to complete the problem. A solution without the appropriate work shown will receive no credit. A solution that is not given in a reasonable order will lose credit. Clearly indicate your answer (for example by enclosing it in a box).

3. It is assumed that your work will begin on the same page as the problem statement. If you choose to begin your work on another page, you must indicate this on the page with the problem statement, with a clear indication of where the work can be found. **If your work continues on to another page, indicate clearly where your work can be found. Failure to indicate this clearly will result in a loss of credit.**

4. Show all units in solutions, intermediate results, and figures. Units in the exam will be included between square brackets.

5. Do not use red ink. Do not use red pencil.

6. You will have 170 minutes to work on this exam.

1. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/30

2. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/30

3. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/30

4. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/30

5. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/40

6. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_/40

Total = 200

Room for extra work

1. {30 Points} Use the circuit below to solve.
2. Find the Thevenin equivalent as seen by the *iS1* current source. Draw the Thevenin equivalent you found, showing numerical values, and attach the *iS1* current source to your equivalent.
3. Find the power delivered by the *iS1* current source in the circuit below.



# Room for extra work

1. {30 Points} The source is a zero mean sinusoidal voltage source with frequency of and the circuit is operating in steady-state.
2. Find the frequency for which the current and the voltage are in phase.
3. Assume for part b) that . Please calculate the reactance and susceptance seen by the voltage source ().



Room for extra work

1. {30 Points}The circuit given below is operating in steady state. It is given that
2. Find the real and reactive power absorbed by the load.
3. Find the reactive power absorbed by the 12[mF] capacitor and the 8[H] inductor, and compare your results with reactive power absorbed by load. Do they have same value and why?
4. Calculate the phase difference between and .
5. Find the power factor and power factor angle of the load. Is it a lagging or leading power factor, and is it an inductive or a capacitive load?



Room for extra work

1. {30 Points} It is given that:
2. Please find the numerical expression of for .
3. Please find the numerical expression of for .
4. Find the energy stored in the equivalent capacitor of the three capacitors at .
5. Find the energy stored in the individual capacitors at.



Room for extra work

1. {40 Points}The switch in the circuit given below had been in position *a* for long time, and then moved to position *b* at *t* = 0.
2. Find *vX*(1[s]).
3. Find the sum of the energies stored in the magnetic fields of the two inductors at *t* = .



Room for extra work

1. {40 Points}The circuit shown operates in steady-state. The current *iA* is given by



Load 1 absorbs 5.7[kW] and delivers 4.3[kVAR].

Load 2 absorbs (6.7-13)[kVA].

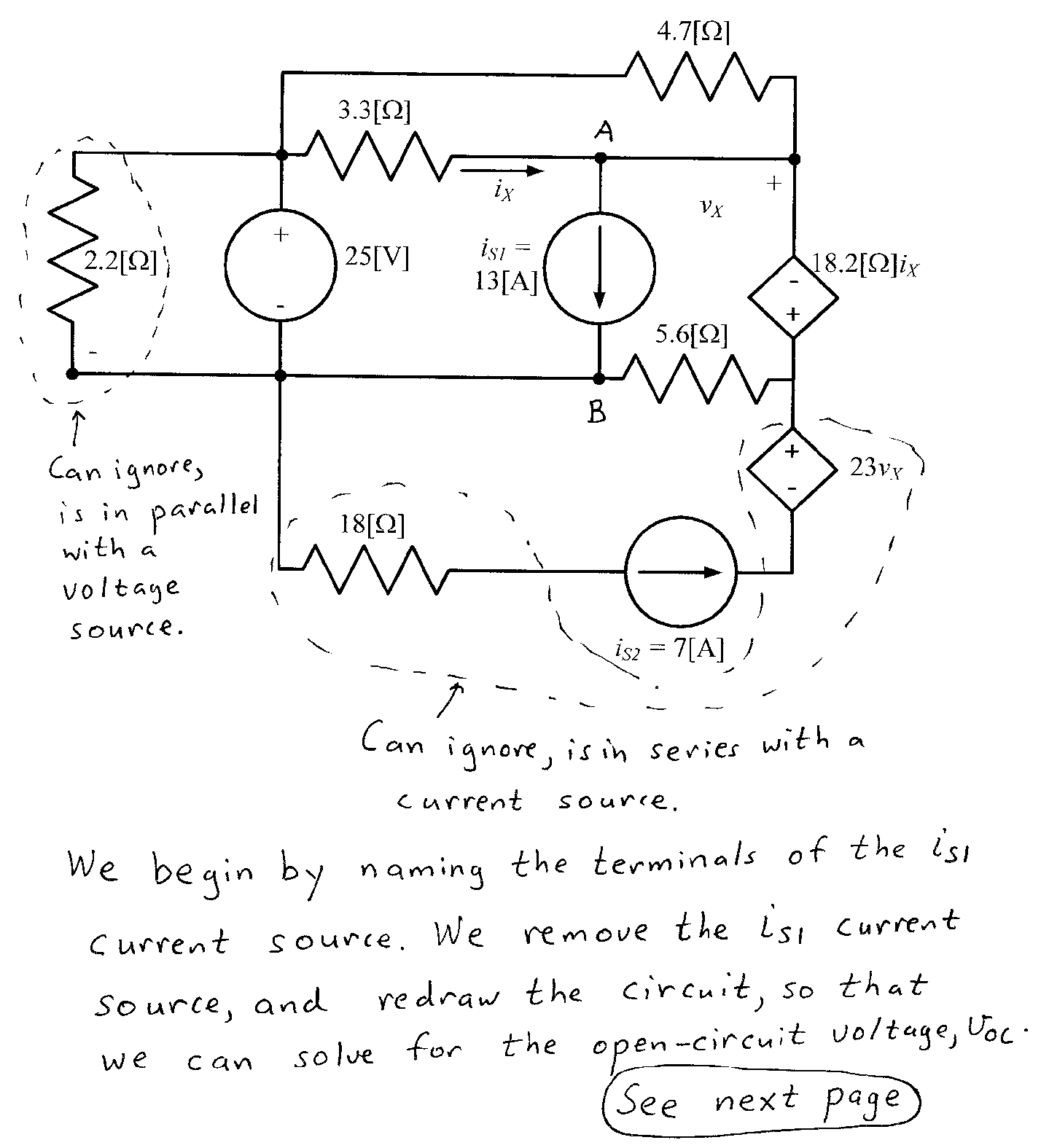
Load 3 absorbs 7.5[kW] at a lagging pf = 0.87.

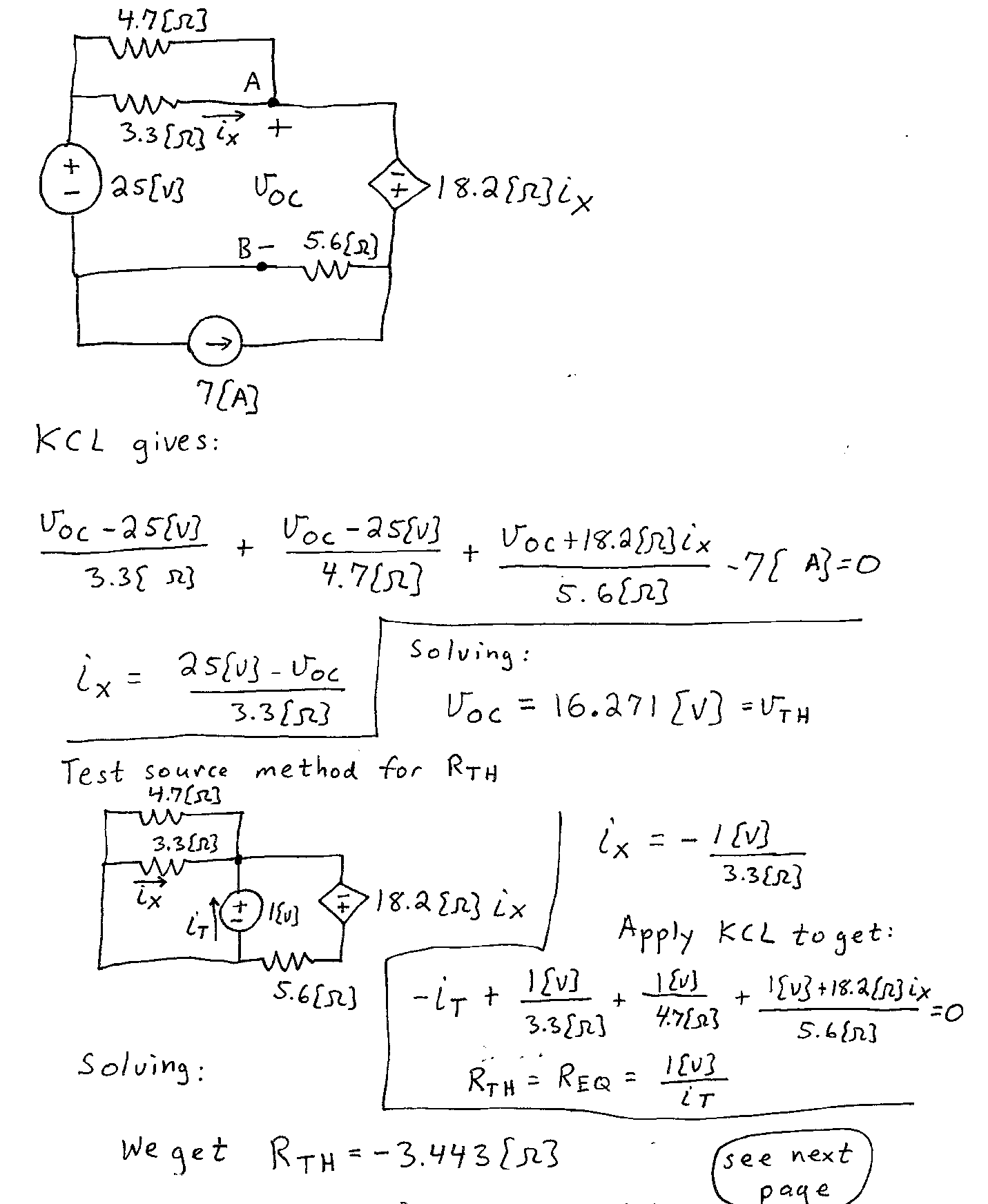
Load 4 absorbs 8.7[kVA] at a leading pf = 0.72.

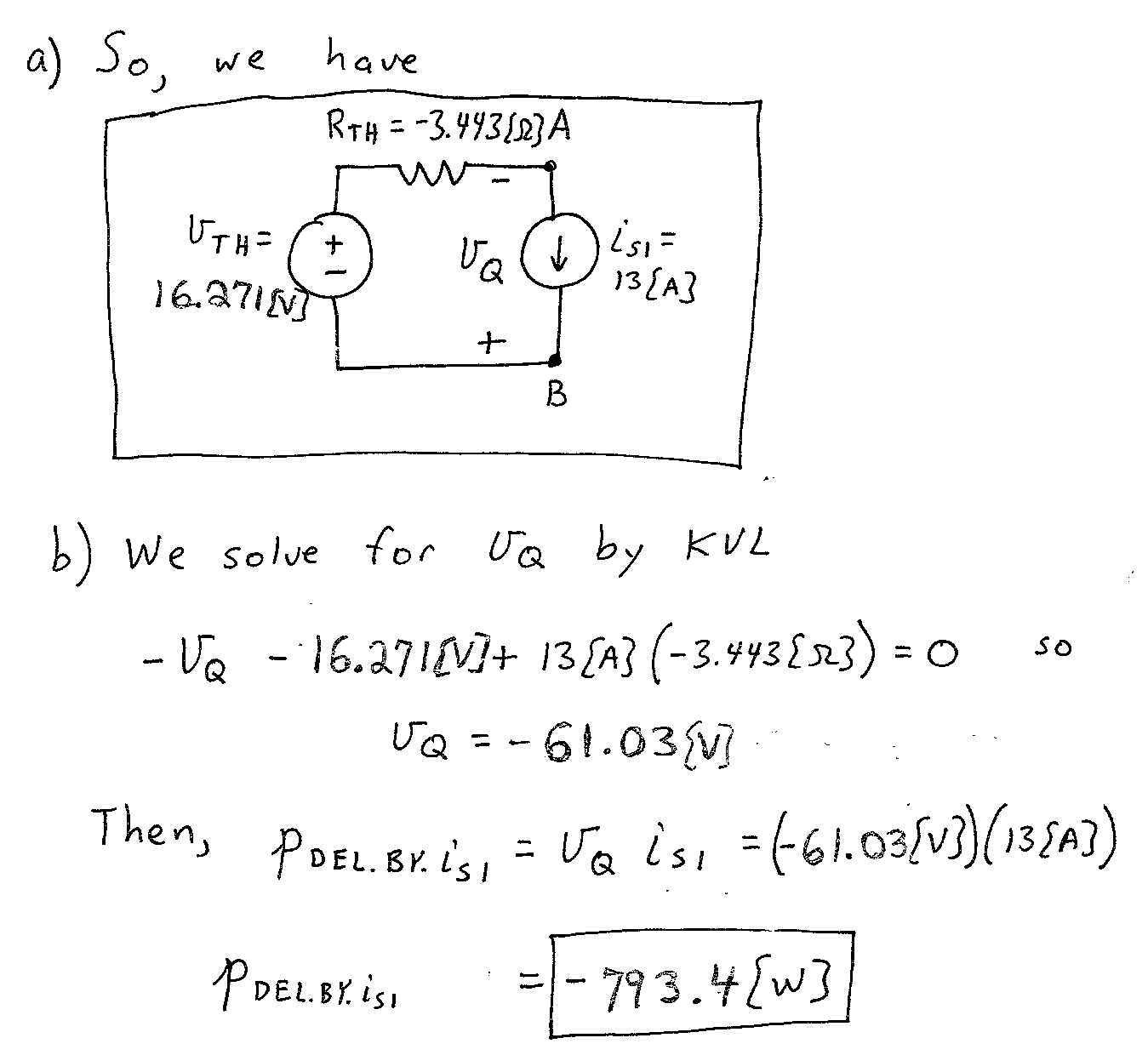
1. Find *vB(t)*.
2. Find the impedance of the combination of the four loads, as seen by the voltage source.
3. Find the power factor of the combination of the four loads, as seen by the voltage source.
4. Find *vL2(t)*.



1. {30 Points} Use the circuit below to solve.
2. Find the Thevenin equivalent as seen by the *iS1* current source. Draw the Thevenin equivalent you found, showing numerical values, and attach the *iS1* current source to your equivalent.
3. Find the power delivered by the *iS1* current source in the circuit below.

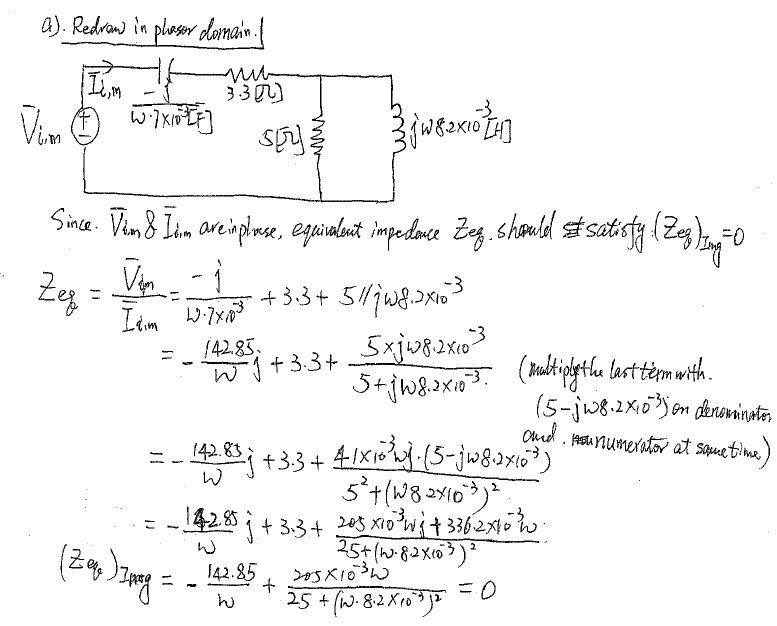


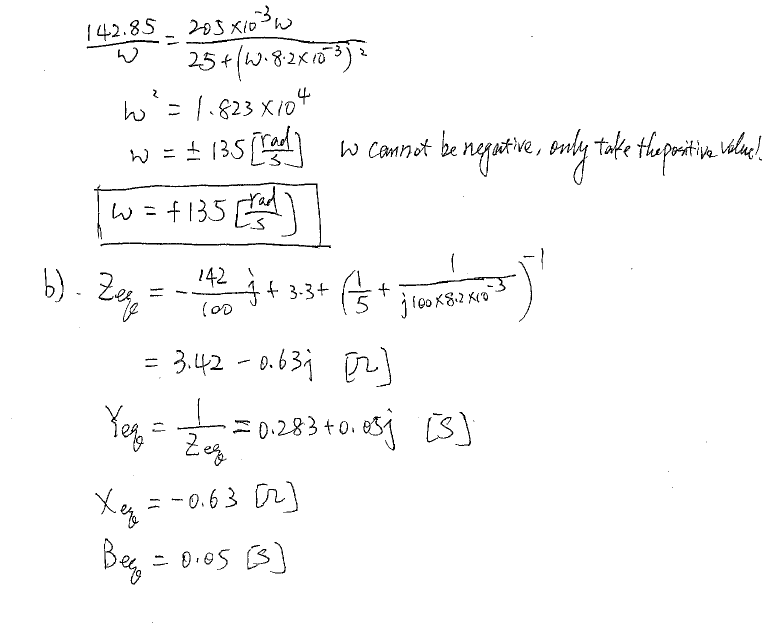




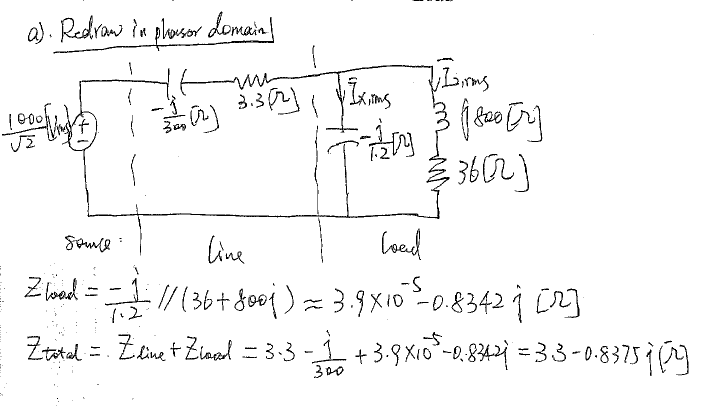
1. {30 Points} The source is a zero mean sinusoidal voltage source with frequency of and the circuit is operating in steady-state.
2. Find the frequency for which the current and the voltage are in phase.
3. Assume for part b) that . Please calculate the reactance and susceptance seen by the voltage source ().

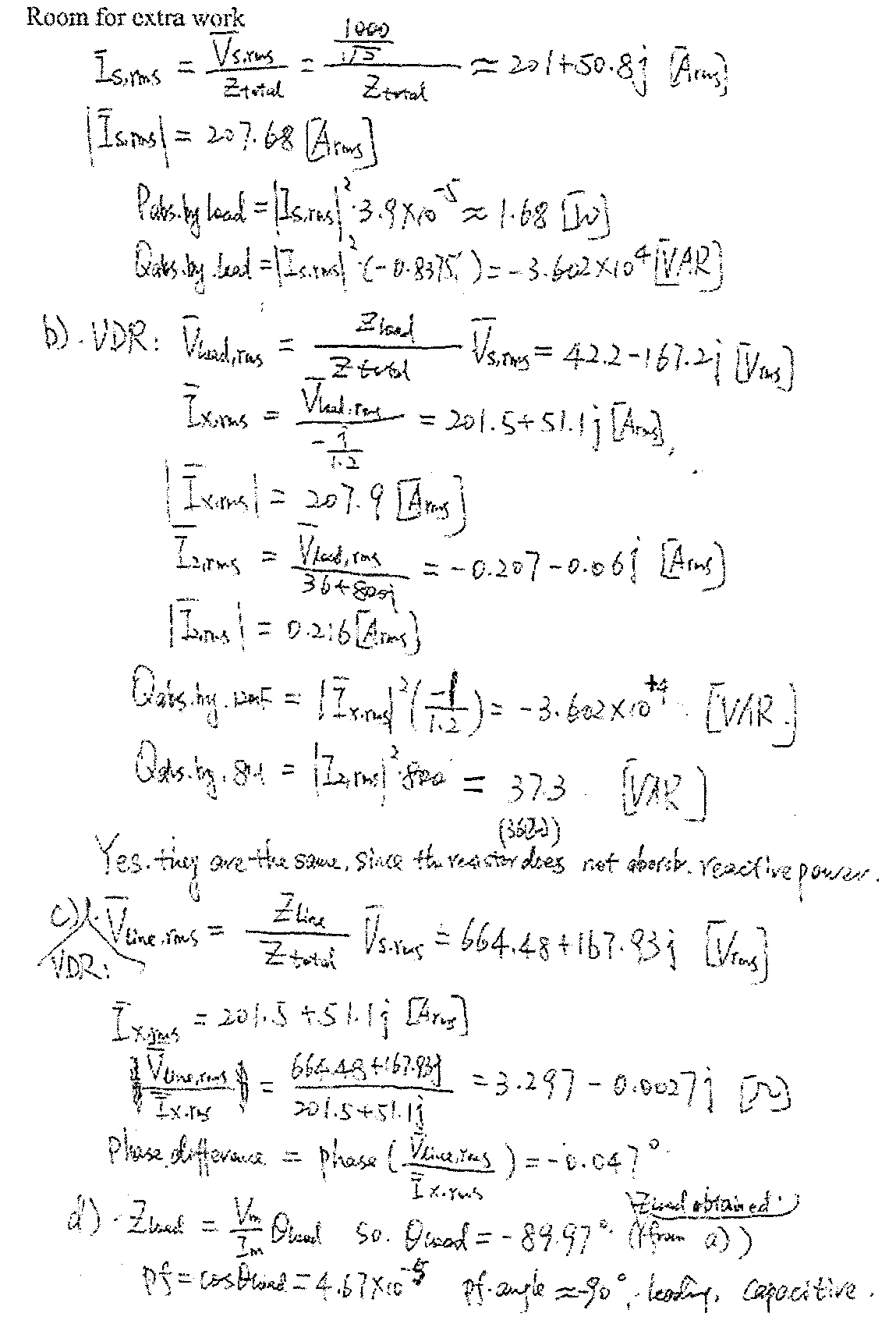






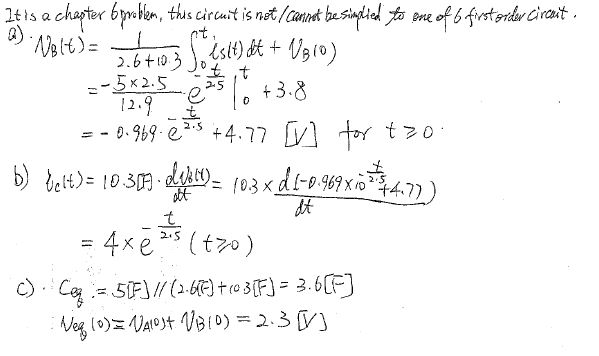
1. {30 Points}The circuit given below is operating in steady state. It is given that
2. Find the real and reactive power absorbed by the load.
3. Find the reactive power absorbed by the 12[mF] capacitor and the 8[H] inductor, and compare your results with reactive power absorbed by load. Do they have same value and why?
4. Calculate the phase difference between and .
5. Find the power factor and power factor angle of the load. Is it a lagging or leading power factor, and is it an inductive or a capacitive load?

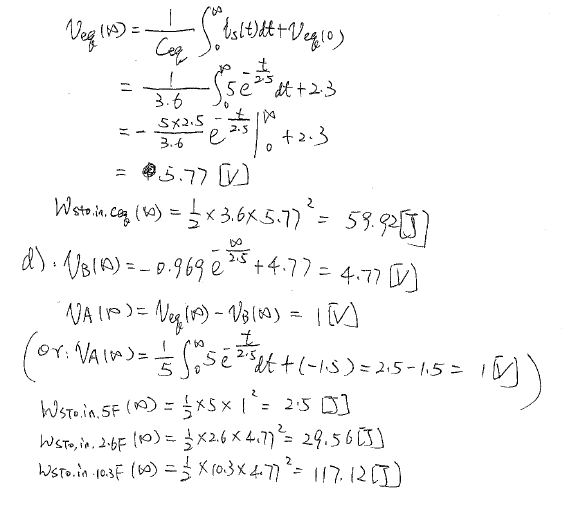




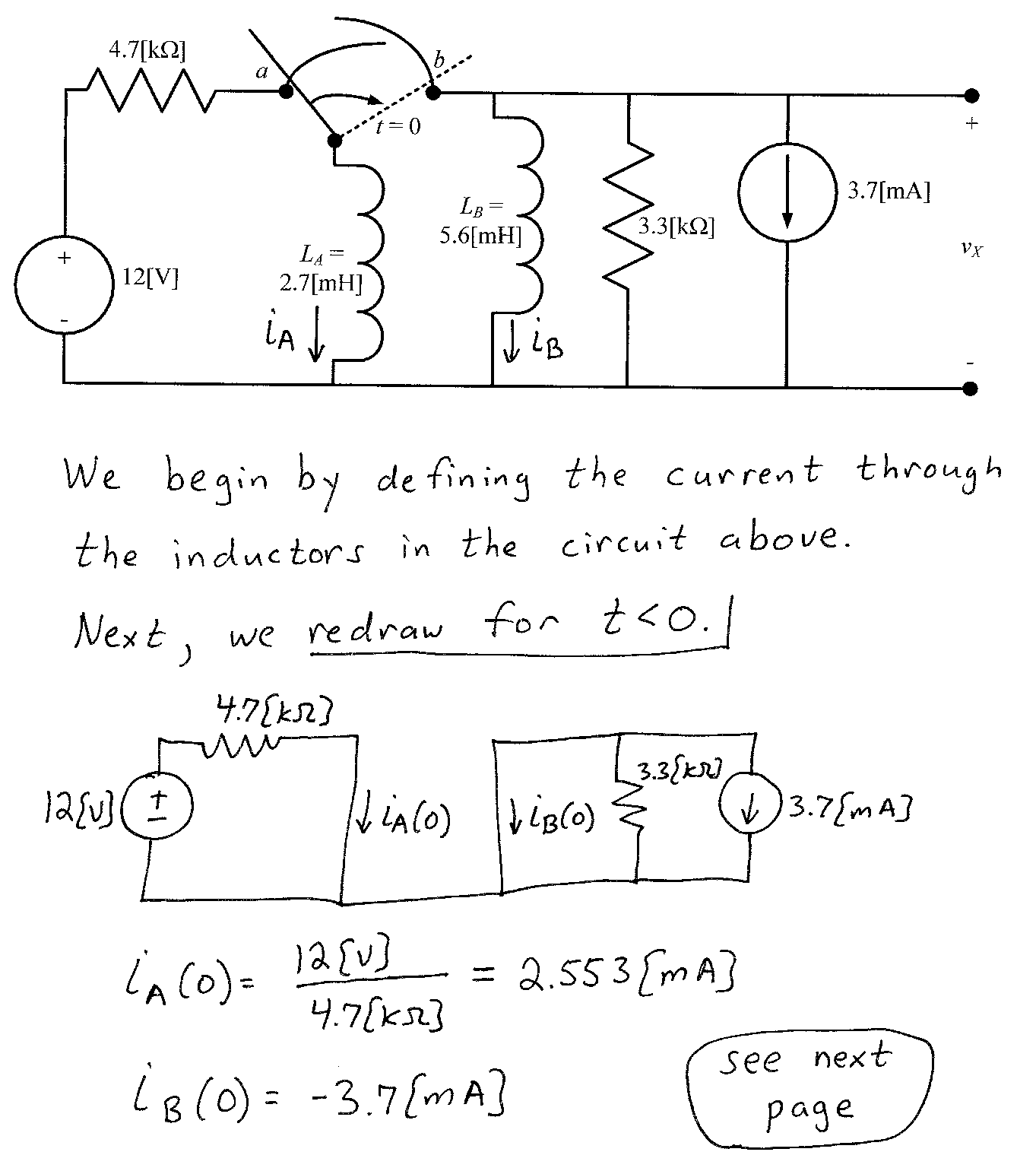
1. {30 Points} It is given that:
2. Please find the numerical expression of for .
3. Please find the numerical expression of for .
4. Find the energy stored in the equivalent capacitor of the three capacitors at .
5. Find the energy stored in the individual capacitors at.

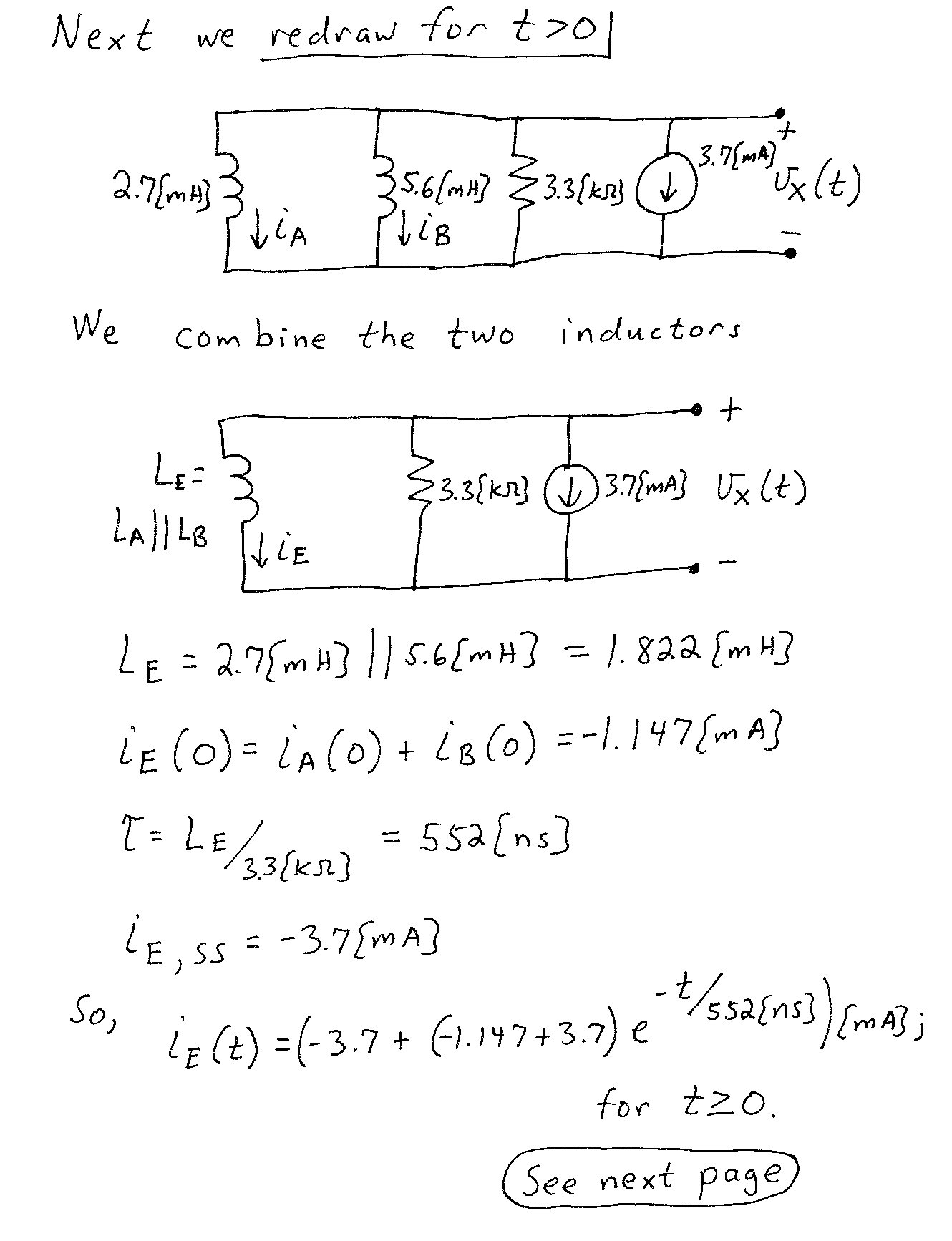


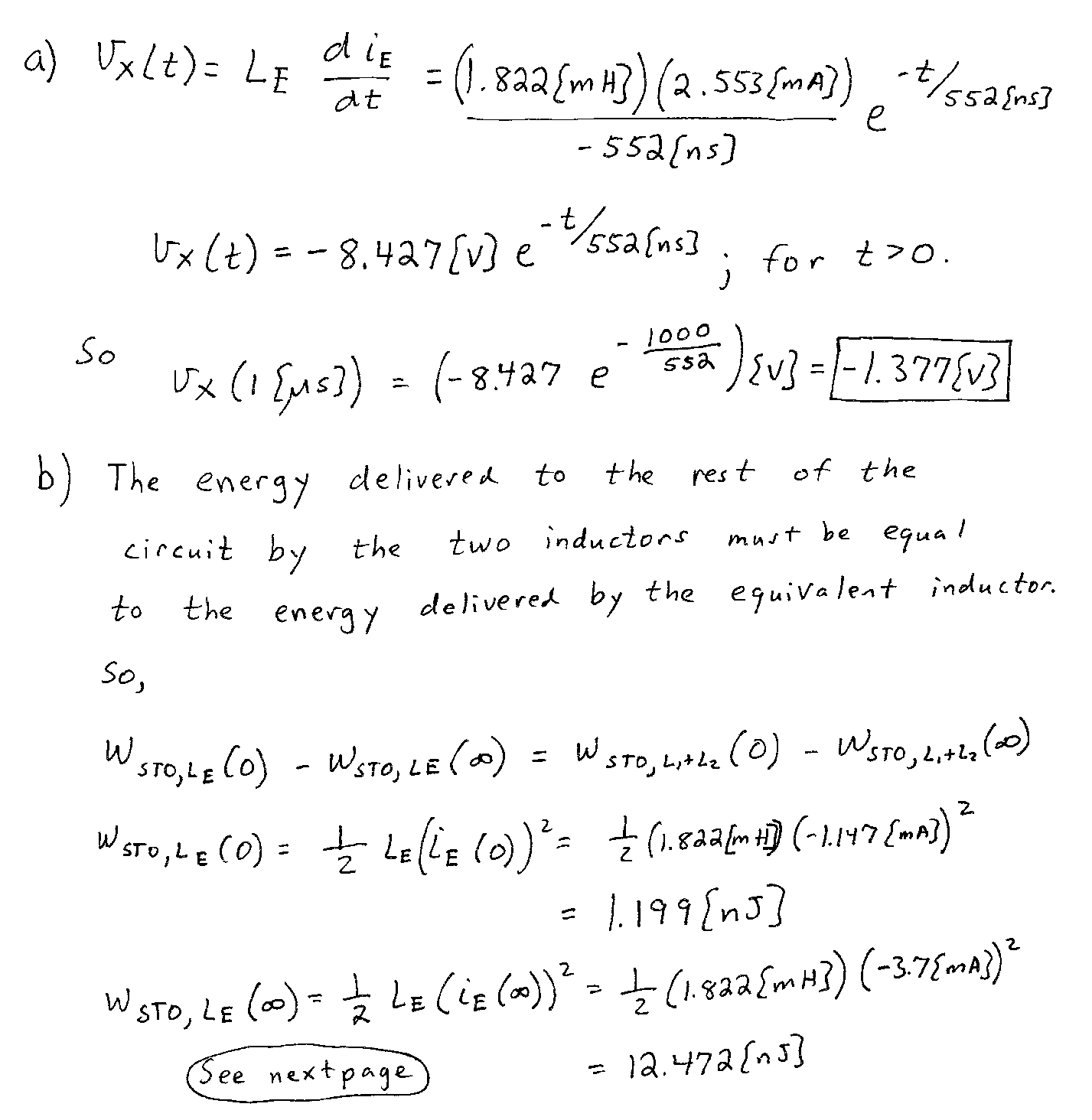


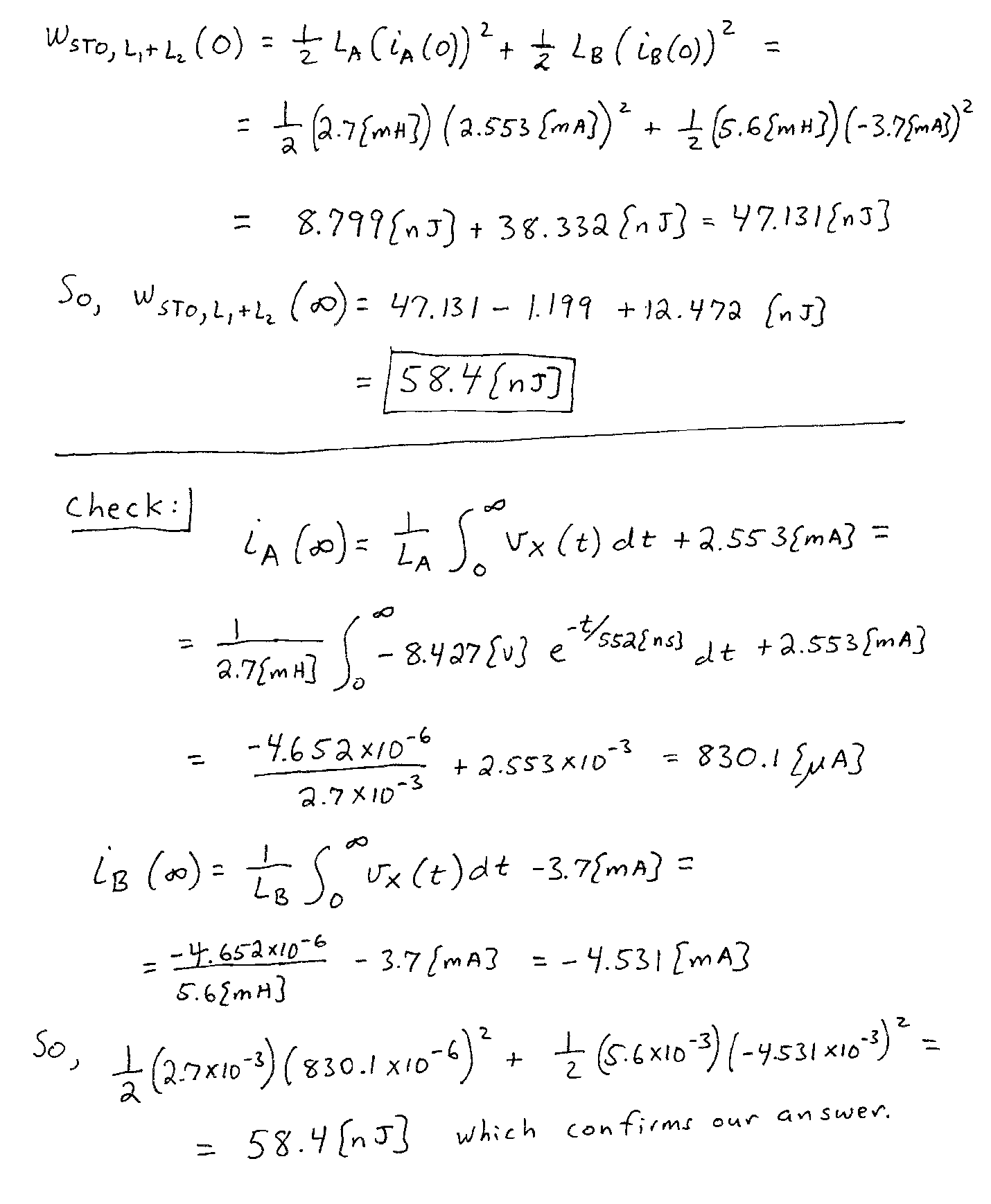


1. {40 Points}The switch in the circuit given below had been in position *a* for long time, and then moved to position *b* at *t* = 0.
2. Find *vX*(1[s]).
3. Find the sum of the energies stored in the magnetic fields of the two inductors at *t* = .









1. {40 Points}The circuit shown operates in steady-state. The current *iA* is given by



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3. Find the power factor of the combination of the four loads, as seen by the voltage source.
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