

# ECE 5317-6351

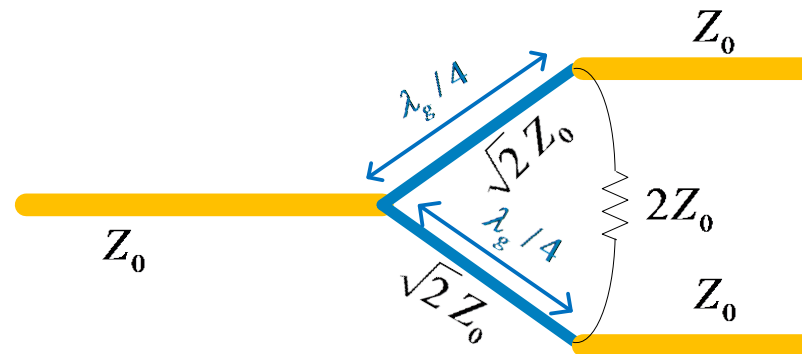
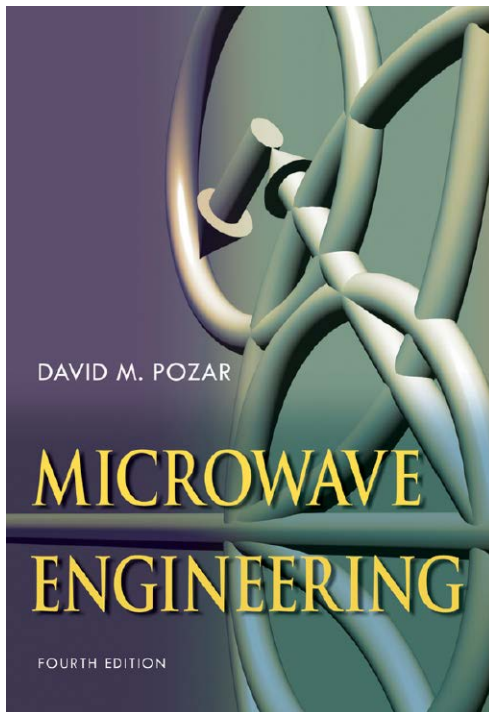
# Microwave Engineering

**Fall 2019**

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Dept. of ECE

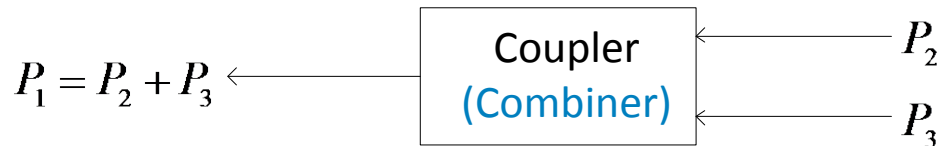
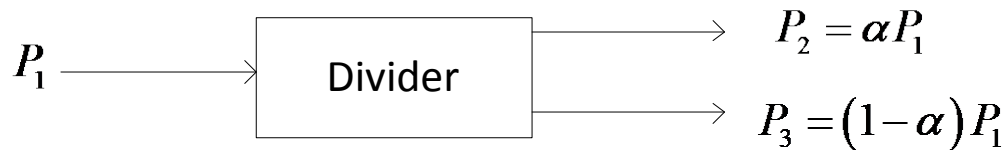
## Notes 20

## Power Dividers and Circulators



# Power Dividers and Couplers

- ❖ A power divider is used to split a signal.
- ❖ A coupler is used to combine a signal.



These are examples of a three-port network.

# Three Port Networks

General 3-port network:

$$[S] = \begin{pmatrix} S_{11} & S_{12} & S_{13} \\ S_{21} & S_{22} & S_{23} \\ S_{31} & S_{32} & S_{33} \end{pmatrix}$$

# Three Port Networks (cont.)

If all three ports are matched, and the device is reciprocal and lossless, we have:

$$[S] = \begin{pmatrix} 0 & S_{12} & S_{13} \\ S_{12} & 0 & S_{23} \\ S_{13} & S_{23} & 0 \end{pmatrix} \quad (\text{The } S \text{ matrix is unitary.})$$

(There are three distinct values.)

Such a device is not physically possible!

(Please see the next slide.)

**Conclusion:** If we want a match at all ports, we have to have a lossy device.

# Power Dividers and Couplers (cont.)

$$[S] = \begin{pmatrix} 0 & S_{12} & S_{13} \\ S_{12} & 0 & S_{23} \\ S_{13} & S_{23} & 0 \end{pmatrix}$$

Lossless, reciprocal, and matched at all ports is not physically possible.

Lossless  $\Rightarrow$   $[S]$  is unitary

Hence:

$$|S_{12}|^2 + |S_{13}|^2 = 1$$

$$|S_{12}|^2 + |S_{23}|^2 = 1$$

$$|S_{13}|^2 + |S_{23}|^2 = 1$$

$$S_{13} S_{23}^* = 0$$

$$S_{12} S_{23}^* = 0$$

$$S_{12} S_{13}^* = 0$$

These cannot all be satisfied.

Only one of them can be nonzero.

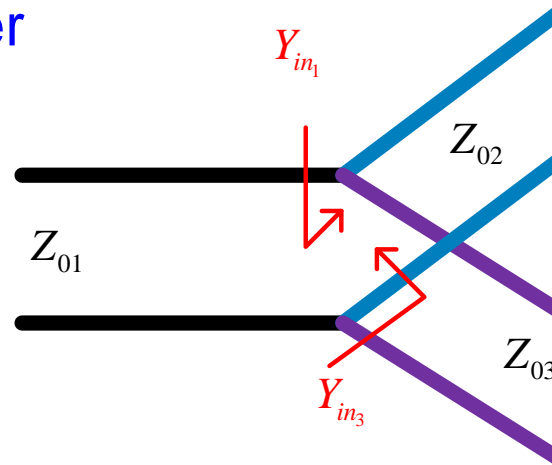
At least 2 of  $S_{13}$ ,  $S_{12}$ ,  $S_{23}$  must be zero.

(If only one is zero (or none is zero), we cannot satisfy all three.)

# Power Dividers

## T-Junction: A lossless divider

$$Y_{in_1} = \frac{1}{Z_{02}} + \frac{1}{Z_{03}}$$



To match:  $Z_{01} = Z_{in_1} = \frac{1}{Y_{in_1}} = Z_{02} \parallel Z_{03} = \frac{Z_{02}Z_{03}}{Z_{02} + Z_{03}}$

We then have:  $Y_{in_3} = \frac{1}{Z_{01}} + \frac{1}{Z_{02}} = \frac{Z_{02} + Z_{03}}{Z_{02}Z_{03}} + \frac{1}{Z_{02}} = \frac{Z_{02} + 2Z_{03}}{Z_{02}Z_{03}} = \frac{1}{Z_{03}} \left( \frac{Z_{02} + 2Z_{03}}{Z_{02}} \right)$

Thus,  $Y_{in_3} \neq \frac{1}{Z_{03}}$

Similarly,  $Y_{in_2} \neq \frac{1}{Z_{02}}$

If we match at port 1, we cannot match at the other ports!

**Note:**

Matching at ports 2 and 3 would be helpful when output lines 2 and 3 are not matched to their loads.

# Power Dividers (cont.)

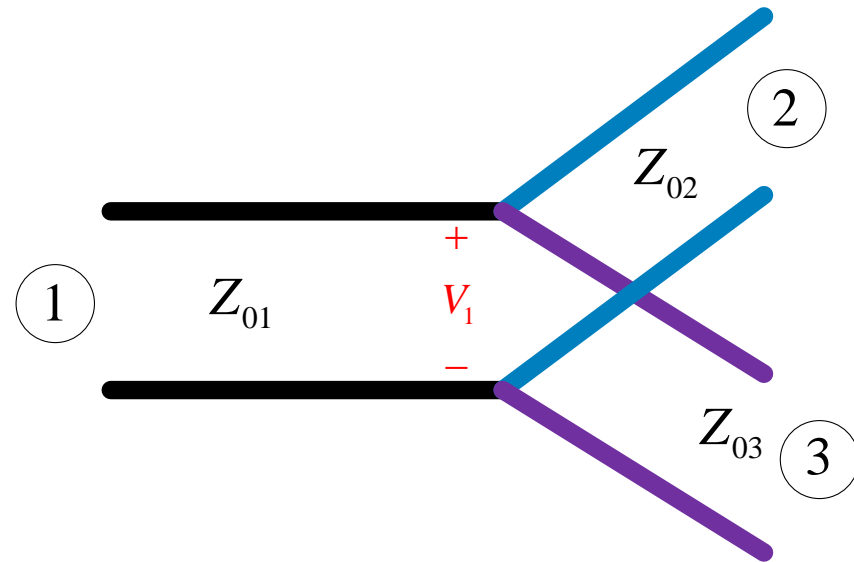
Assuming port 1 matched:

$$Z_{01} = \frac{Z_{02}Z_{03}}{Z_{02} + Z_{03}}$$

$$P_{in_1} = \frac{1}{2} \frac{|V_1|^2}{Z_{01}}$$

$$P_{out_2} = \frac{1}{2} \frac{|V_1|^2}{Z_{02}} = \frac{Z_{01}}{Z_{02}} P_{in_1}$$

$$P_{out_3} = \frac{1}{2} \frac{|V_1|^2}{Z_{03}} = \frac{Z_{01}}{Z_{03}} P_{in_1}$$



$$\frac{P_{out_2}}{P_{out_3}} = \frac{Z_{03}}{Z_{02}}$$

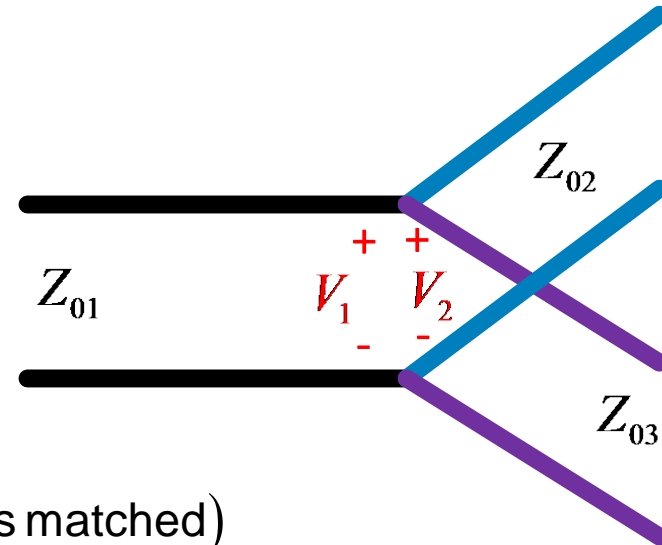
We can design the splitter to control the powers going into the two output lines.

# Power Dividers (cont.)

Examine the reflection at each port ( $S_{ii}$ ):

$$S_{11} = \frac{V_1^- / \sqrt{Z_{01}}}{V_1^+ / \sqrt{Z_{01}}} \Big|_{a_2=a_3=0} = \frac{V_1^-}{V_1^+} \Big|_{a_2=a_3=0}$$

$$= \frac{Z_{in1} - Z_{01}}{Z_{in1} + Z_{01}} = \frac{Z_{02} \parallel Z_{03} - Z_{01}}{Z_{02} \parallel Z_{03} + Z_{01}} \quad (\text{zero if port 1 is matched})$$



$$S_{22} = \frac{V_2^-}{V_2^+} \Big|_{a_1=a_3=0}$$

$$= \frac{Z_{01} \parallel Z_{03} - Z_{02}}{Z_{01} \parallel Z_{03} + Z_{02}}$$

$$S_{33} = \frac{V_3^-}{V_3^+} \Big|_{a_1=a_2=0}$$

$$= \frac{Z_{01} \parallel Z_{02} - Z_{03}}{Z_{01} \parallel Z_{02} + Z_{03}}$$

**Note:** A match on port 1 requires:

$$Z_{01} < Z_{02}, \quad Z_{01} < Z_{03}$$

(The two output lines combine in parallel.)

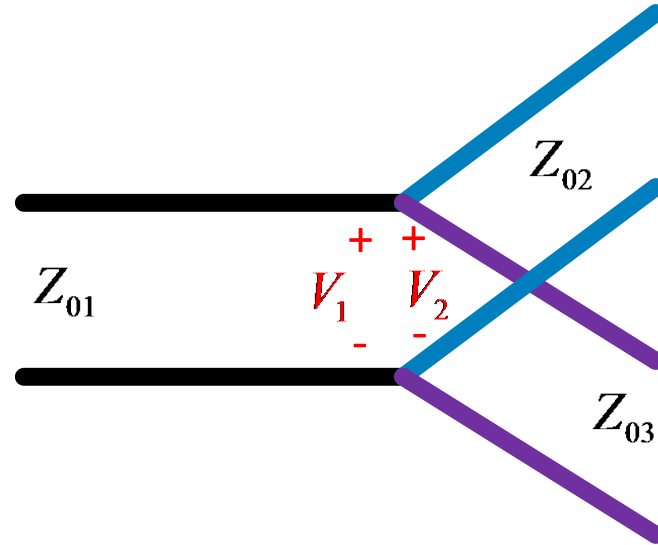
The first term in the numerators must be less than the second term.  $\Rightarrow S_{22} \neq 0, \quad S_{33} \neq 0$



# Power Dividers (cont.)

Also, we have:

$$S_{21} = \left. \frac{\frac{V_2^-}{\sqrt{Z_{02}}}}{\frac{V_1^+}{\sqrt{Z_{01}}}} \right|_{a_2=a_3=0} = \frac{V_2^-}{V_1^+} \sqrt{\frac{Z_{01}}{Z_{02}}}$$



$$V_2^- / V_1^+ = V_2 / V_1^+ = V_1 / V_1^+$$

Also

$$V_1 = V_1^+ (1 + S_{11}) \Rightarrow V_1 / V_1^+ = (1 + S_{11})$$

Hence

$$S_{21} = (1 + S_{11}) \sqrt{\frac{Z_{01}}{Z_{02}}} = S_{12}$$

Similarly:

$$S_{31} = (1 + S_{11}) \sqrt{\frac{Z_{01}}{Z_{03}}} = S_{13}$$

$$S_{32} = S_{23} = (1 + S_{22}) \sqrt{\frac{Z_{02}}{Z_{03}}}$$

# Power Dividers (cont.)

If port 1 is matched:

$$Z_{01} = \frac{Z_{02}Z_{03}}{Z_{02} + Z_{03}}$$

$$\Rightarrow S_{11} = 0 ; \quad S_{22} = \frac{Z_{01} \parallel Z_{03} - Z_{02}}{Z_{01} \parallel Z_{03} + Z_{02}} ; \quad S_{33} = \frac{Z_{01} \parallel Z_{02} - Z_{03}}{Z_{01} \parallel Z_{02} + Z_{03}}$$

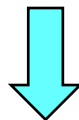
$$S_{21} = S_{12} = (1 + S_{11}) \sqrt{\frac{Z_{01}}{Z_{02}}} = \sqrt{\frac{Z_{01}}{Z_{02}}} = \sqrt{\frac{Z_{03}}{Z_{02} + Z_{03}}}$$

$$S_{31} = S_{13} = (1 + S_{11}) \sqrt{\frac{Z_{01}}{Z_{03}}} = \sqrt{\frac{Z_{01}}{Z_{03}}} = \sqrt{\frac{Z_{02}}{Z_{02} + Z_{03}}}$$

$$[S] = \begin{pmatrix} 0 & S_{12} & S_{13} \\ S_{12} & S_{22} & S_{23} \\ S_{13} & S_{23} & S_{33} \end{pmatrix}$$

Only  $S_{11} = 0$

From last slide:  $S_{32} = S_{23} = (1 + S_{22}) \sqrt{\frac{Z_{02}}{Z_{03}}}$

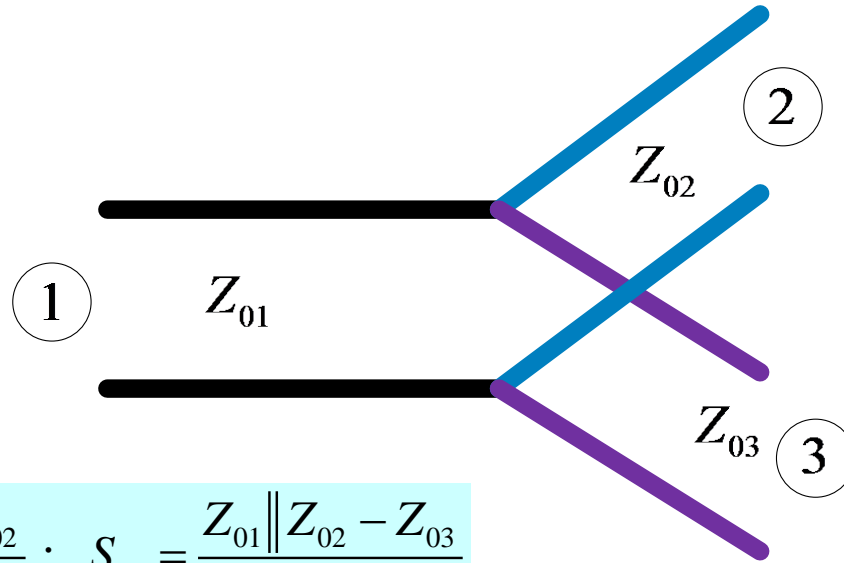


The output ports 2 and 3 are not isolated.

# Power Dividers (cont.)

## Summary

$$Z_{01} = \frac{Z_{02} Z_{03}}{Z_{02} + Z_{03}}$$



$$\frac{P_{\text{out}3}}{P_{\text{out}2}} = \frac{Z_{02}}{Z_{03}}$$

$$S_{11} = 0 ; \quad S_{22} = \frac{Z_{01} \parallel Z_{03} - Z_{02}}{Z_{01} \parallel Z_{03} + Z_{02}} ; \quad S_{33} = \frac{Z_{01} \parallel Z_{02} - Z_{03}}{Z_{01} \parallel Z_{02} + Z_{03}}$$

$$S_{21} = S_{12} = \sqrt{\frac{Z_{03}}{Z_{02} + Z_{03}}}$$

$$S_{31} = S_{13} = \sqrt{\frac{Z_{02}}{Z_{02} + Z_{03}}}$$

$$S_{32} = S_{23} = (1 + S_{22}) \sqrt{\frac{Z_{02}}{Z_{03}}}$$

- The input port is matched, but not the output ports.
- The output ports are not isolated.



Waves reflected from devices on ports 2 and 3 with cause interference with the other devices.

# Power Dividers (cont.)

## Example: Microstrip T-junction power divider

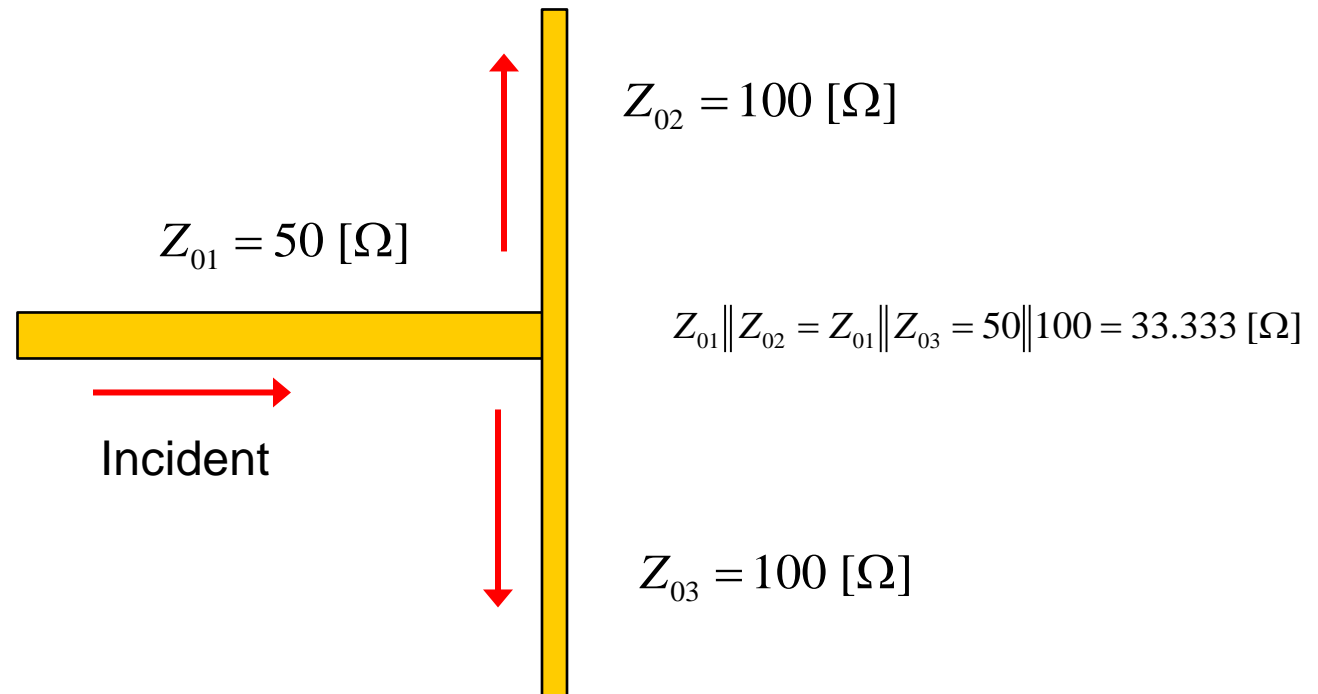
$$S_{11} = 0$$

$$S_{22} = S_{33} = -\frac{1}{2}$$

$$S_{21} = S_{12} = \sqrt{\frac{1}{2}}$$

$$S_{31} = S_{13} = \sqrt{\frac{1}{2}}$$

$$S_{32} = S_{23} = \frac{1}{2}$$



**Note:**

Quarter-wave transformers could be put on the output lines to bring the final output lines back to 50 [Ω].

# Power Dividers (cont.)

The matched power divider also works as a matched power combiner.

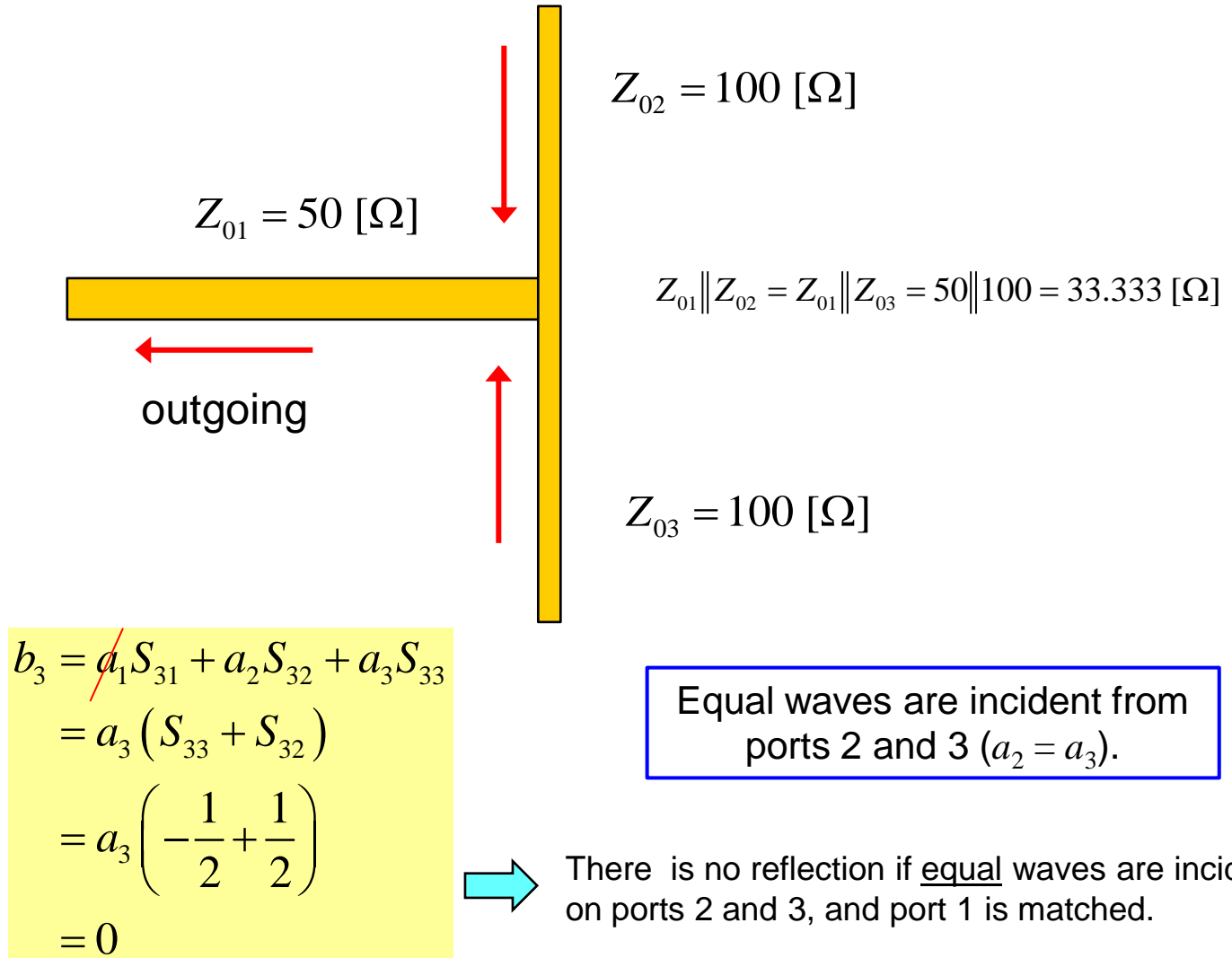
$$S_{11} = 0$$

$$S_{22} = S_{33} = -\frac{1}{2}$$

$$S_{21} = S_{12} = \sqrt{\frac{1}{2}}$$

$$S_{31} = S_{13} = \sqrt{\frac{1}{2}}$$

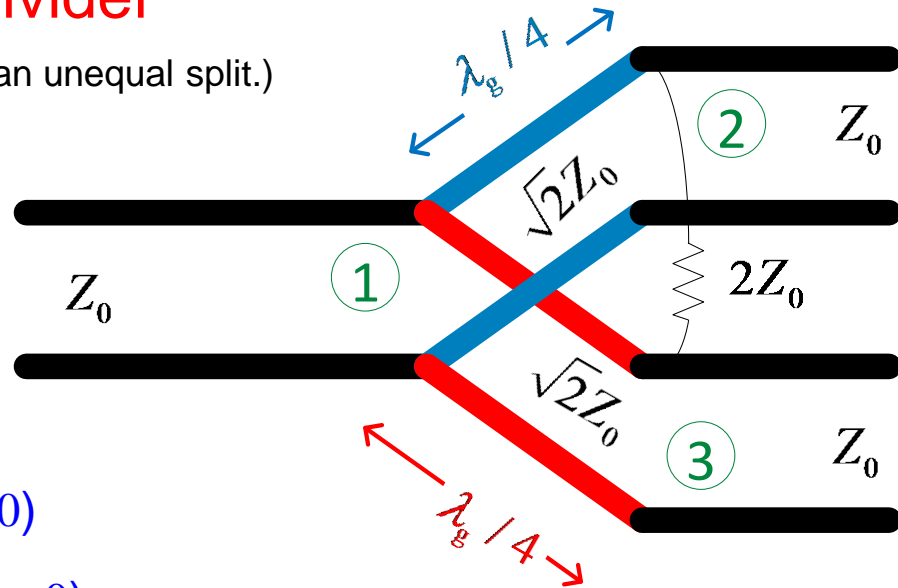
$$S_{32} = S_{23} = \frac{1}{2}$$



# Wilkinson Power Divider

## Equal-split (3 dB) power divider

(The Wilkinson can also be designed to have an unequal split.)



- All ports matched ( $S_{11} = S_{22} = S_{33} = 0$ )
- Output ports are isolated ( $S_{23} = S_{32} = 0$ )

**Note:** No power is lost in going from port 1 to ports 2 and 3:

$$|S_{21}|^2 = |S_{31}|^2 = \frac{1}{2}$$

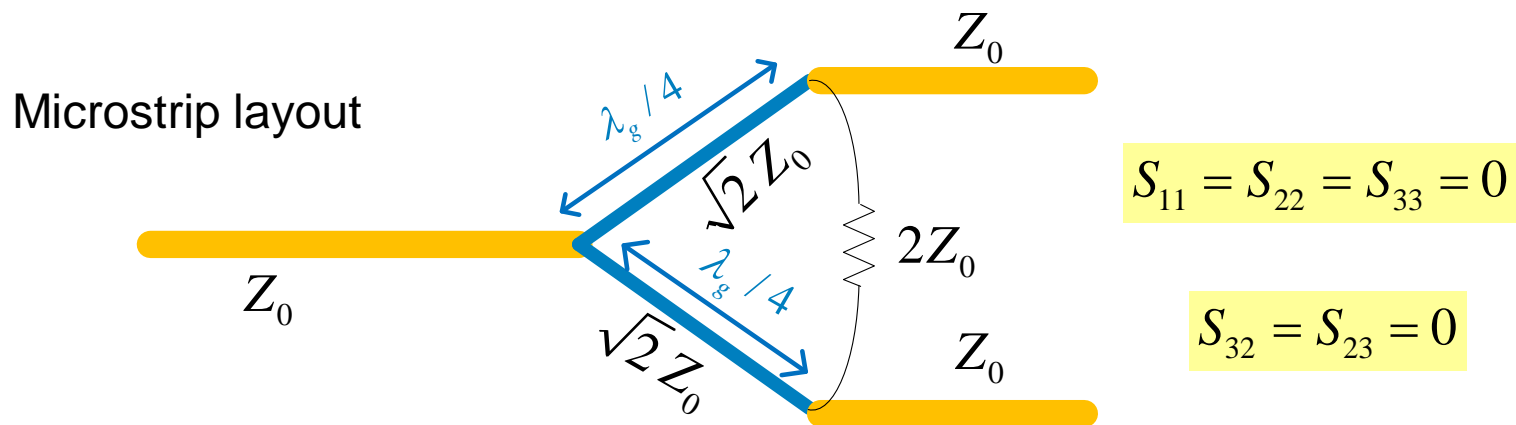
$$[S] = \frac{-j}{\sqrt{2}} \begin{pmatrix} 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 0 & 0 \end{pmatrix}$$

The derivation is in the appendix.

Obviously not unitary

# Wilkinson Power Divider (cont.)

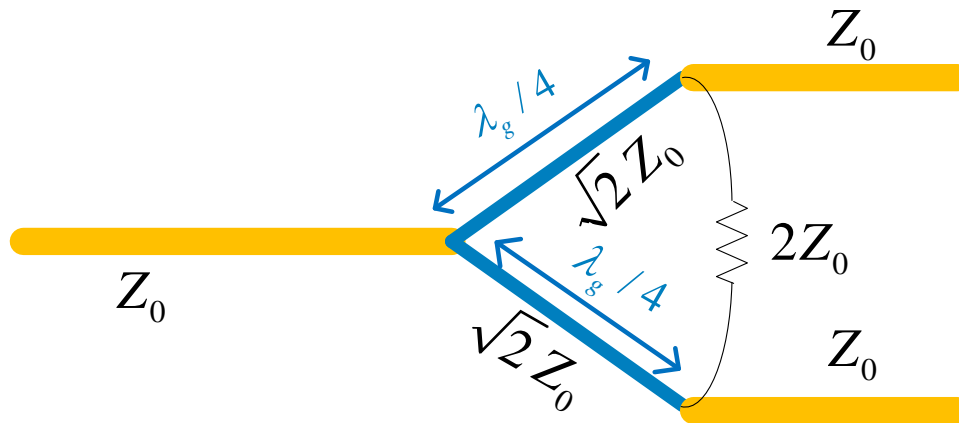
$$[S] = \frac{-j}{\sqrt{2}} \begin{pmatrix} 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 0 & 0 \end{pmatrix}$$



All three ports are matched, and the output ports are isolated.

# Wilkinson Power Divider (cont.)

$$[S] = \frac{-j}{\sqrt{2}} \begin{pmatrix} 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 0 & 0 \end{pmatrix}$$



$$S_{21} = S_{31} = \frac{-j}{\sqrt{2}}$$

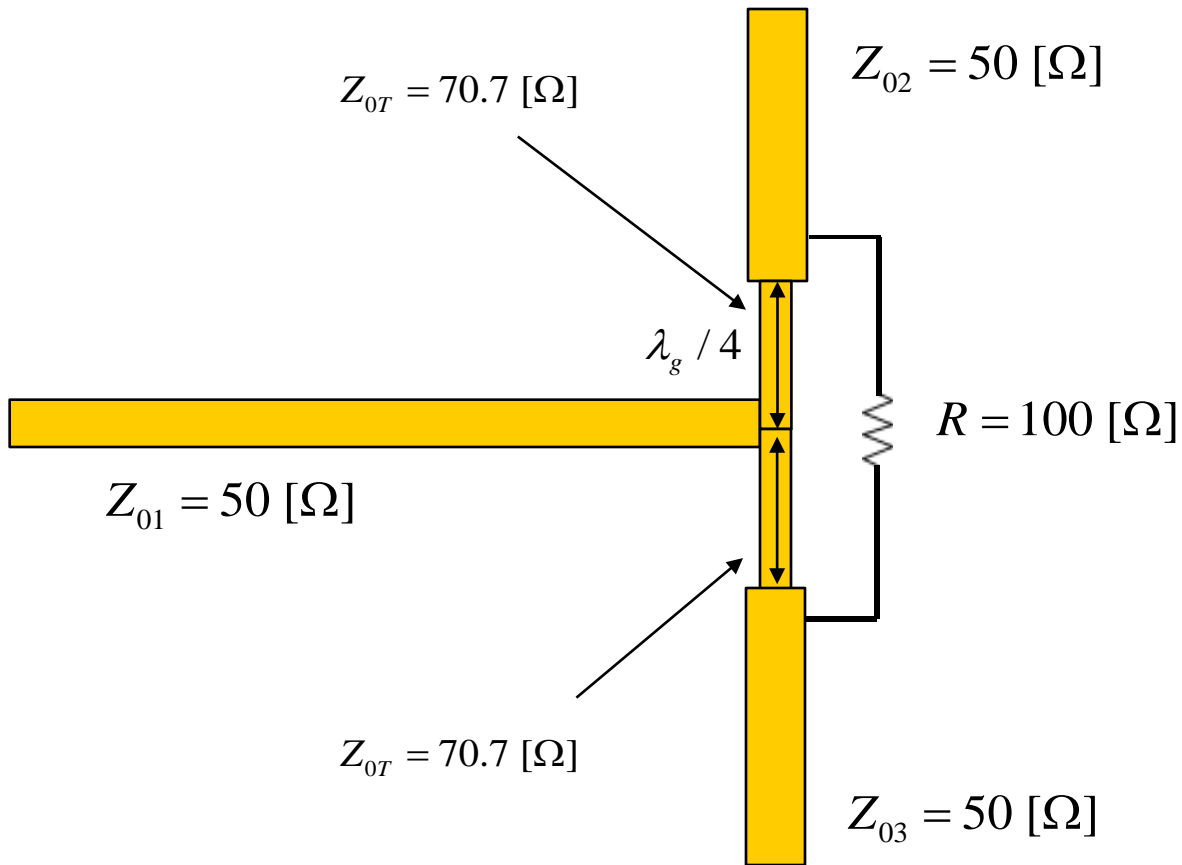
$$S_{12} = S_{13} = \frac{-j}{\sqrt{2}}$$

- When a wave is incident from port 1, half of the total incident power gets transmitted to each output port (no loss of power).
- When a wave is incident from port 2 or port 3, half of the power gets transmitted to port 1 and half gets absorbed by the resistor, but nothing gets through to the other output port (the two output ports are isolated from each other).



# Wilkinson Power Divider (cont.)

## Example: Microstrip Wilkinson power divider

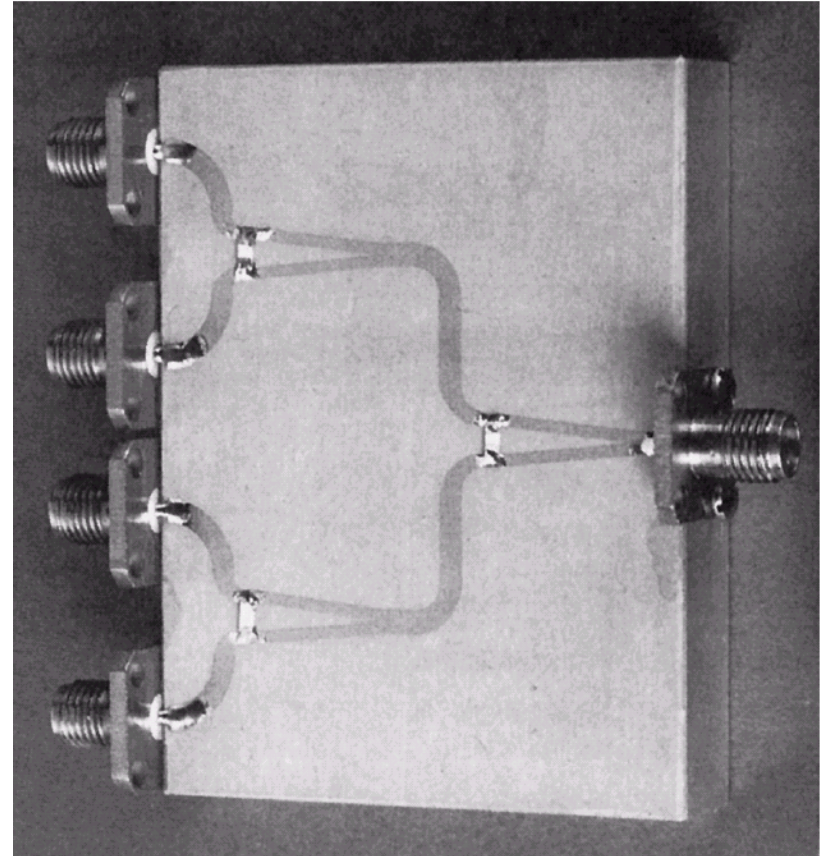


# Wilkinson Power Divider (cont.)

Figure 7.15 of Pozar

Photograph of a four-way corporate power divider network using three microstrip Wilkinson power dividers. Note the isolation chip resistors.

Courtesy of M.D. Abouzahra, MIT Lincoln Laboratory.



# Wilkinson Power Divider (cont.)

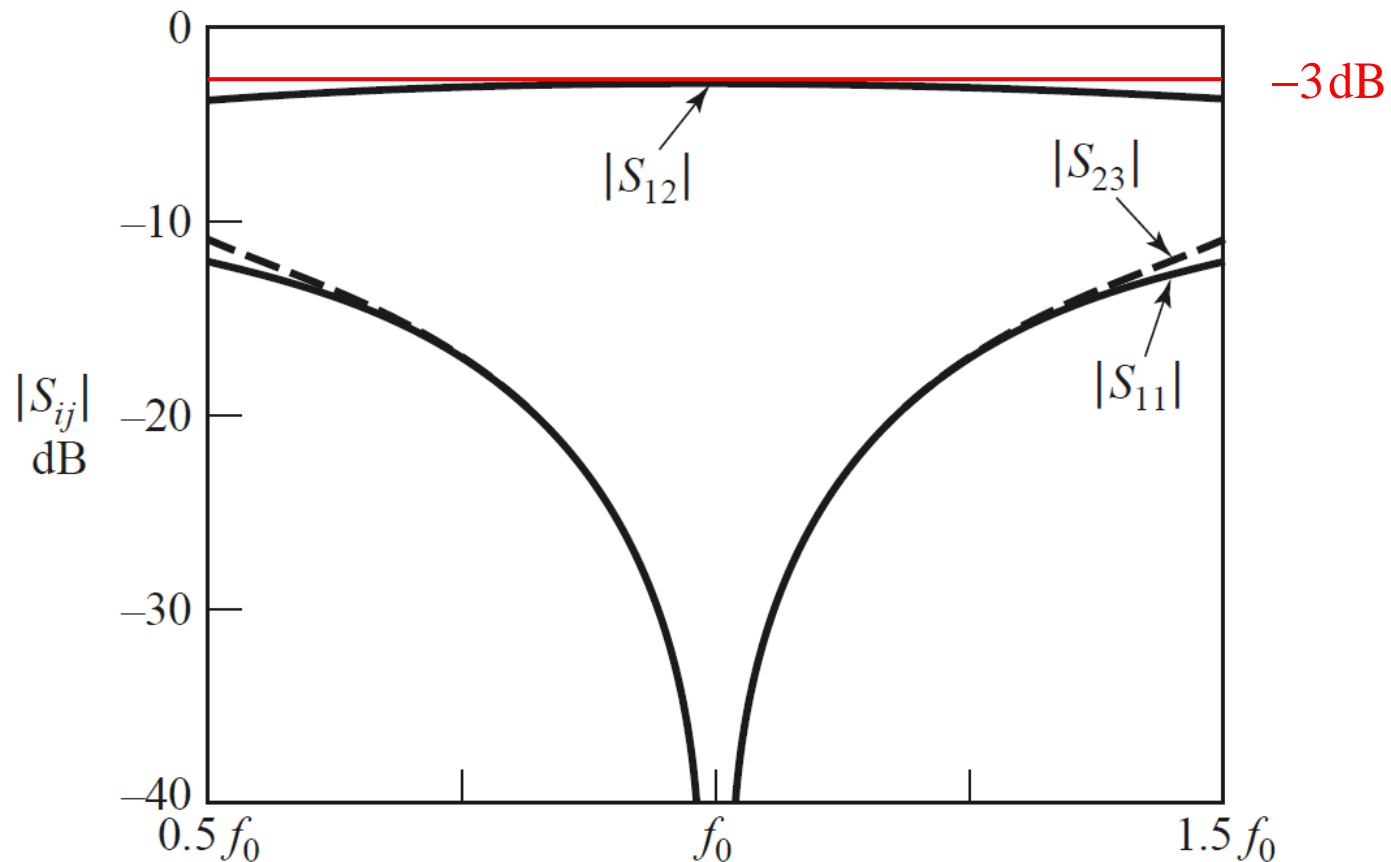


Figure 7.12 of Pozar

Frequency response of an equal-split Wilkinson power divider. Port 1 is the input port; ports 2 and 3 are the output ports.

# Circulators

Now consider a 3-port network that is **non-reciprocal**, with all ports matched, and is lossless:

$$\Rightarrow [S] = \begin{pmatrix} 0 & S_{12} & S_{13} \\ S_{21} & 0 & S_{23} \\ S_{31} & S_{32} & 0 \end{pmatrix}$$

$$S_{ij} \neq S_{ji}$$

“Circulator”



(There are six distinct values.)

These equations will be satisfied if:

Lossless  
(unitary)

$$\Rightarrow |S_{21}|^2 + |S_{31}|^2 = 1$$

$$|S_{12}|^2 + |S_{32}|^2 = 1$$

$$|S_{13}|^2 + |S_{23}|^2 = 1$$

$$S_{31} S_{32}^* = 0$$

$$S_{21} S_{23}^* = 0$$

$$S_{12} S_{13}^* = 0$$

$$\textcircled{1} \quad S_{12} = S_{23} = S_{31} = 0$$

$$|S_{21}| = |S_{32}| = |S_{13}| = 1$$

or

$$\textcircled{2} \quad S_{21} = S_{32} = S_{13} = 0$$

$$|S_{12}| = |S_{23}| = |S_{31}| = 1$$

# Circulators (cont.)

①

$$[S] = \begin{pmatrix} 0 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{pmatrix}$$

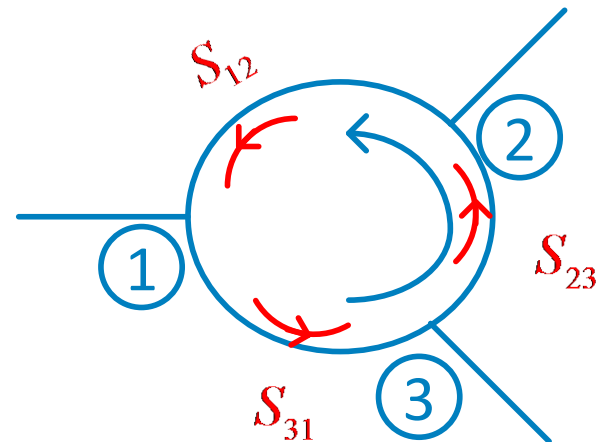
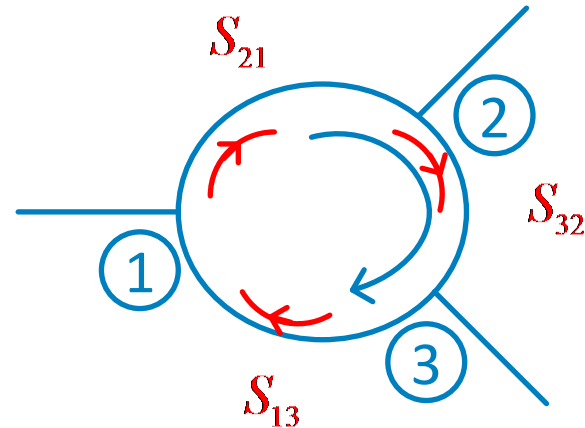
**Note:** We have assumed here that the phases of all the  $S$  parameters are zero.

②

$$[S] = \begin{pmatrix} 0 & 1 & 0 \\ 0 & 0 & 1 \\ 1 & 0 & 0 \end{pmatrix}$$

A wave goes in one port and comes out from the adjacent port!

Clockwise (LH) circulator



Counter-clockwise (RH) circulator

# Circulators (cont.)

An example of a circulator:



Frequency: 0.698 – 0.96 GHz

VSWR: <1.3

Isolation : >18 dB

Power: 1000 W

Insertion loss: <0.35 dB

Length: 1.75 in

Width: 2 in

Height: 1.02 in

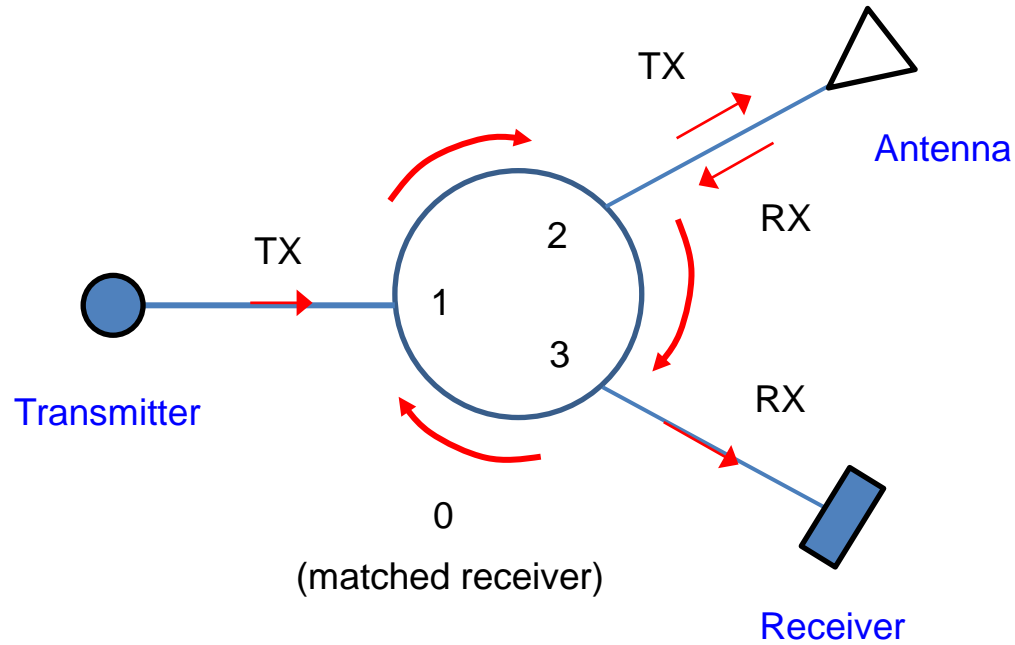
Temperature range: -20 – 70 deg C

Price: \$439.94

Circulators can be made using biased ferrite materials.

# Circulators (cont.)

## Application: Wireless system



- ❖ The same antenna can be used for transmit and receive.
- ❖ The transmit and receive frequencies can even be the same.

**Note:** A circulator used this way is often called a duplexer.

# Duplexer

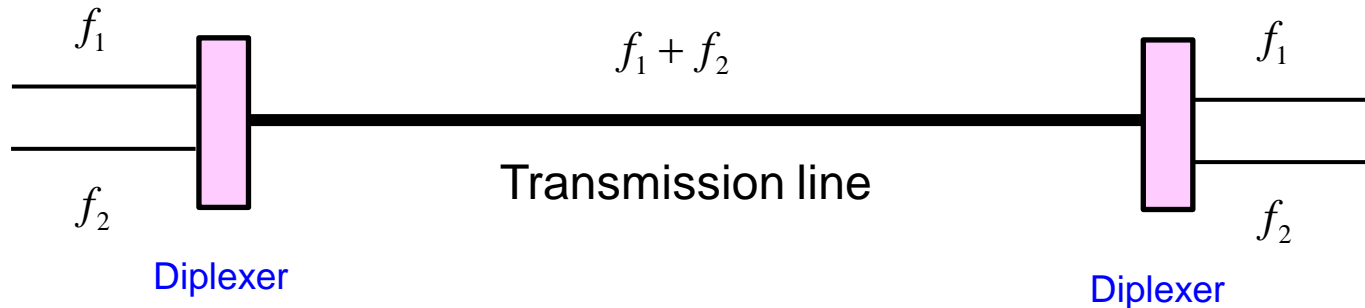
A duplexer:





# Diplexer

A diplexer) is a type of filter that combines or splits two different frequencies ( $f_1$  and  $f_2$ ).

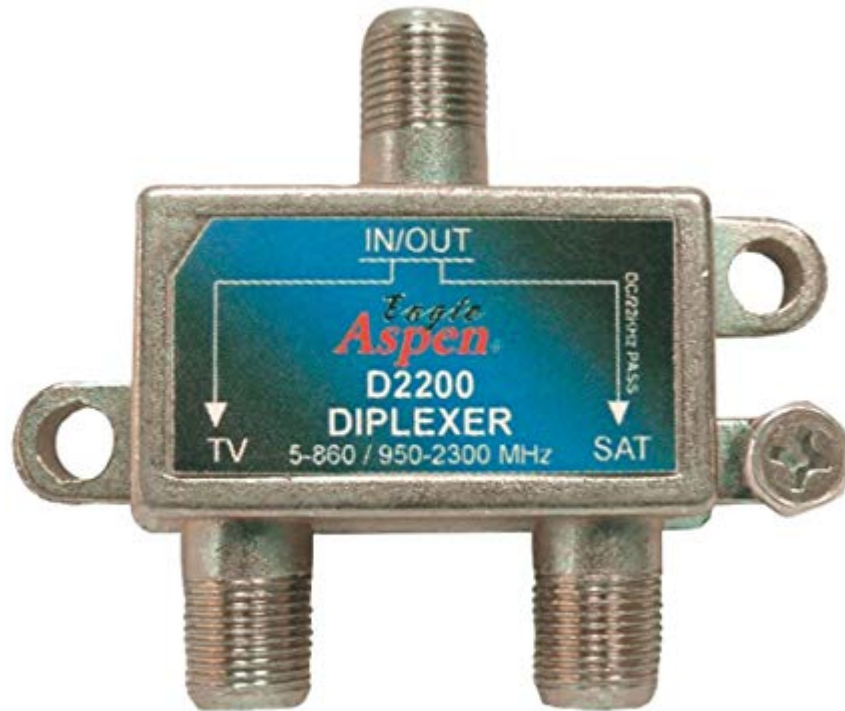


Triplexer: three frequencies

Multiplexer: multiple frequencies

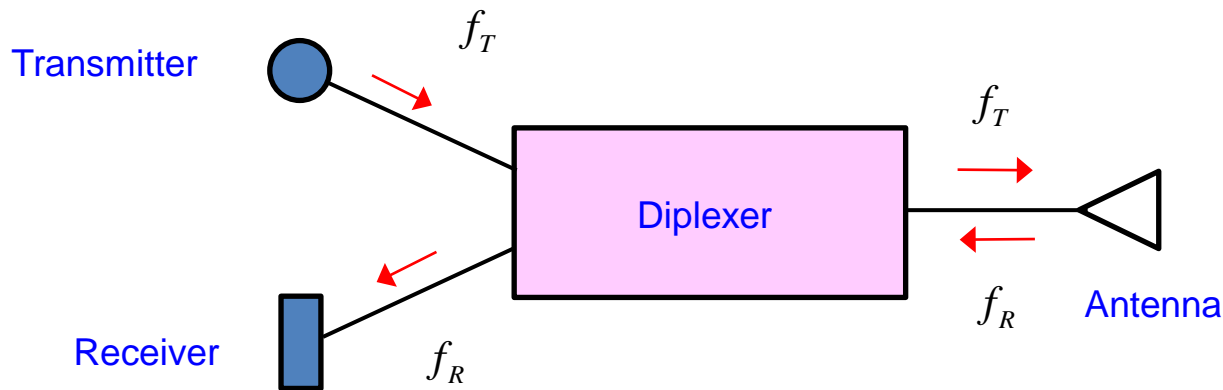
# Diplexer (cont.)

An example of a diplexer:



# Diplexer (cont.)

**Note:** A diplexer can be used to transmit and receive two different channels with the same antenna (as with a duplexer), if the frequencies are separated enough.



**Note:**

This requires a high isolation between the transmit and receive ports, to avoid interference.

# Isolator

An isolator is a two-port device that is nonreciprocal:

$$[S] = \begin{pmatrix} 0 & 0 \\ 1 & 0 \end{pmatrix}$$

- ❖ A wave coming in on port 1 goes through to port 2.
- ❖ A wave coming in on port 2 gets absorbed (does not go to port 1).



Frequency: 0.8 - 1 GHz

VSWR: 1.25

Isolation : 20 dB

Power: 2 W

Insertion loss: <0.4 dB

Length: 1.25 in

Width: 2 in

Height: 0.75 in

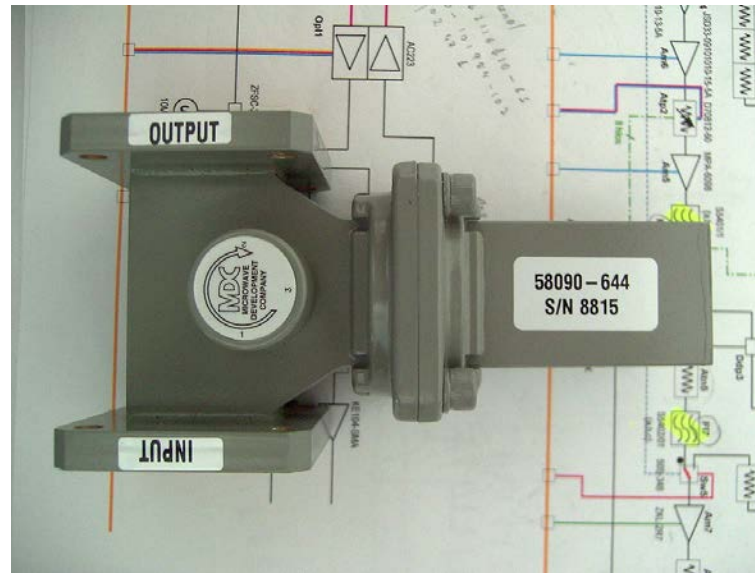
Temperature range: -20 – 60 deg C

Price: \$279.82



# Circulator as Isolator (cont.)

A waveguide-based circulator with a matched load at port 3, acting as an isolator



<https://en.wikipedia.org/wiki/Circulator>

# Appendix

**The analysis of the Wilkinson power divider is given here.**

- ❖ Even/odd mode analysis is used to analyze the Wilkinson power divider.
- ❖ This requires two separate analyzes (even and odd).
- ❖ Each analysis involves only a two-port device instead of a three-port device.

# Appendix (cont.)

- Even and odd analysis is used to analyze the structure when port 2 is excited.

⇒ To determine  $S_{22}, S_{32}$

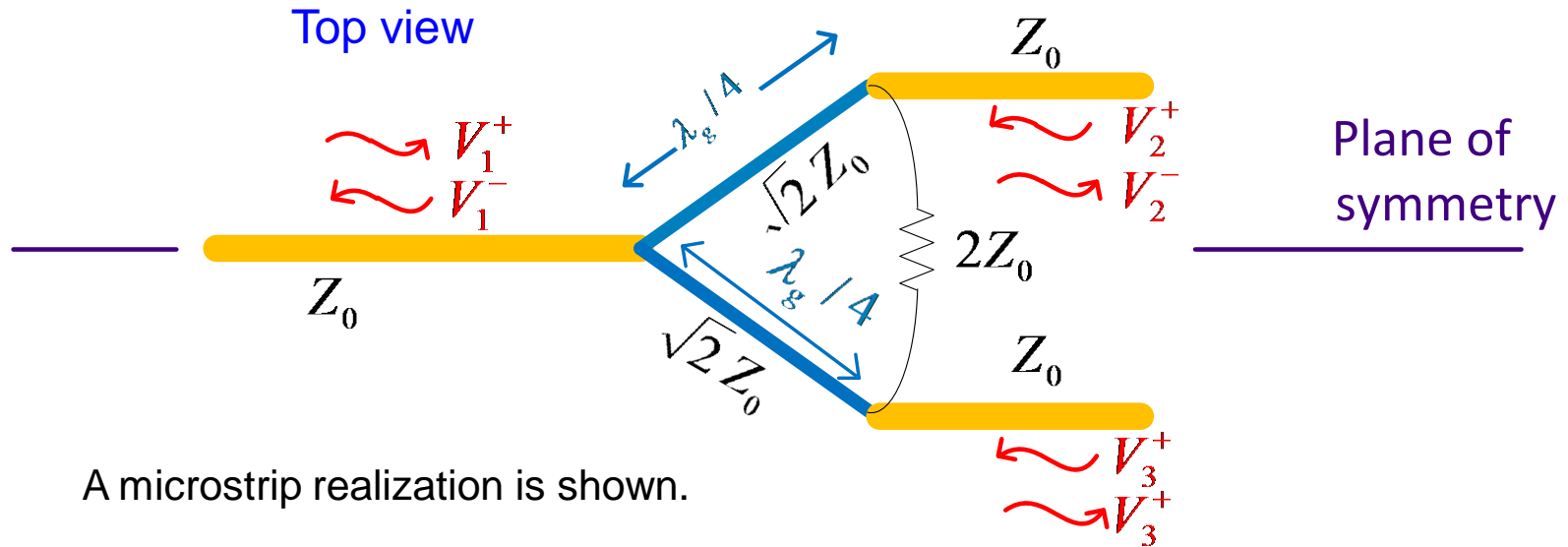
- Only even analysis is needed to analyze the structure when port 1 is excited.

⇒ To determine  $S_{11}, S_{21}$

The other five components of the  $S$  matrix can be found by using physical symmetry and reciprocity (the symmetry of the  $S$  matrix).



# Appendix (cont.)

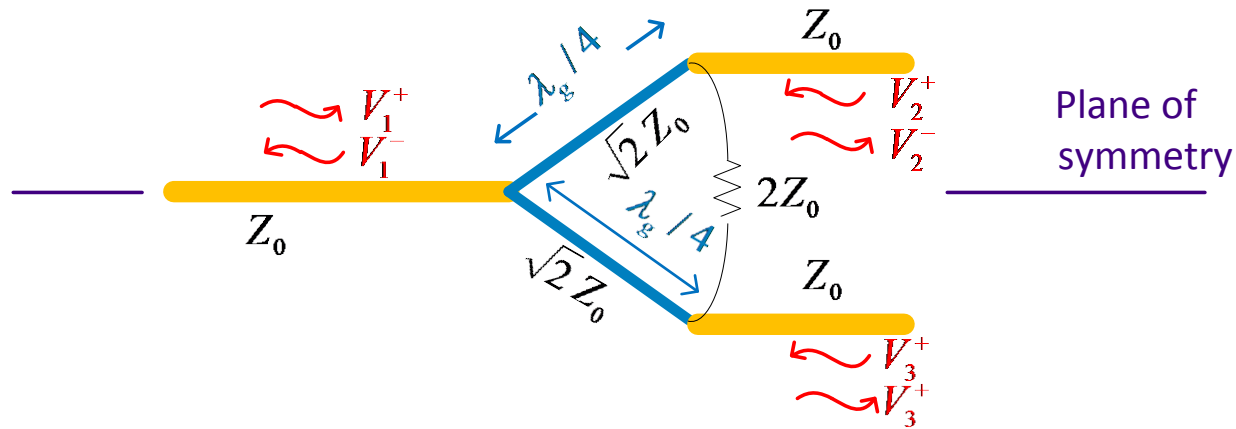


## Split structure along plane of symmetry (POS)

Even  $\Rightarrow$  voltage even about POS  $\Rightarrow$  place OC along POS

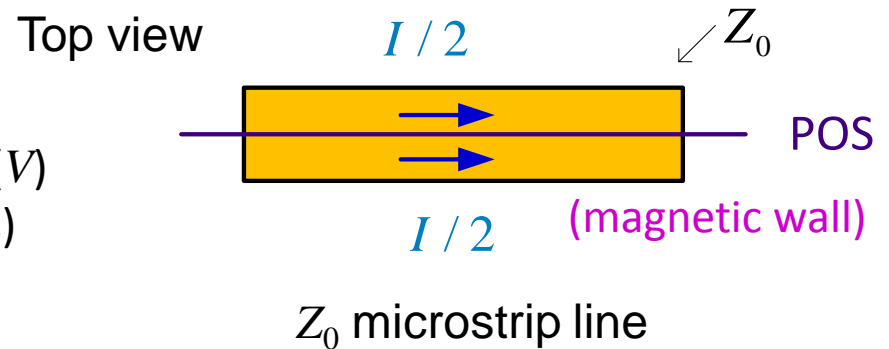
Odd  $\Rightarrow$  voltage odd about POS  $\Rightarrow$  place SC along POS

# Appendix (cont.)



How do you split a transmission line? (This is needed for the even case.)

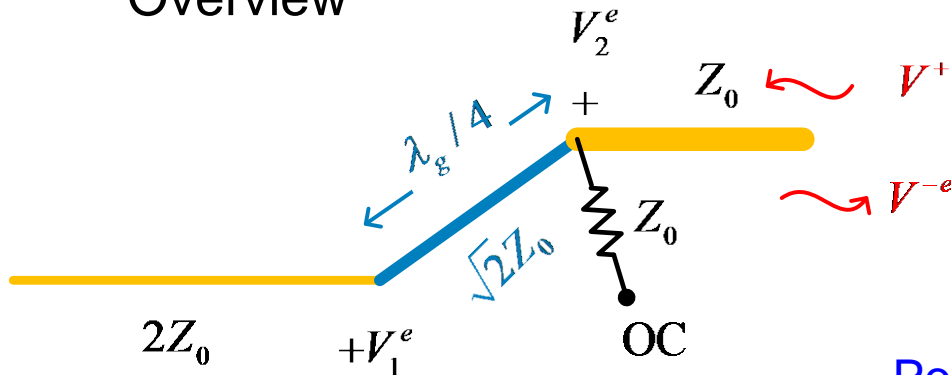
Voltage is the same for each half of line ( $V$ )  
 Current is halved for each half of line ( $I/2$ )



For each half  $\Rightarrow Z_0^h = \frac{V}{I/2} = 2Z_0$

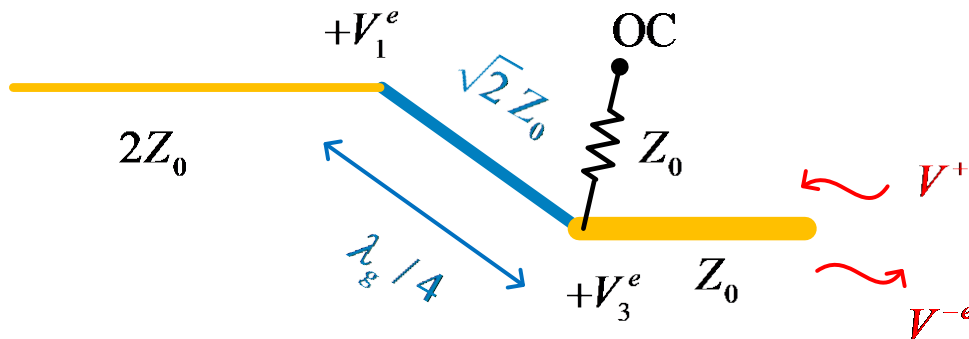
# Appendix (cont.)

Port 2 Excitation  
 “even” problem  
 Overview



**Note:**  
 The  $2Z_0$  resistor has been split into two  $Z_0$  resistors in series.

Ports 2 and 3 are excited in phase.



$$\Rightarrow S_{22}^e, S_{32}^e$$

$$S_{22}^e \equiv \frac{V^{-e}}{V^+}$$

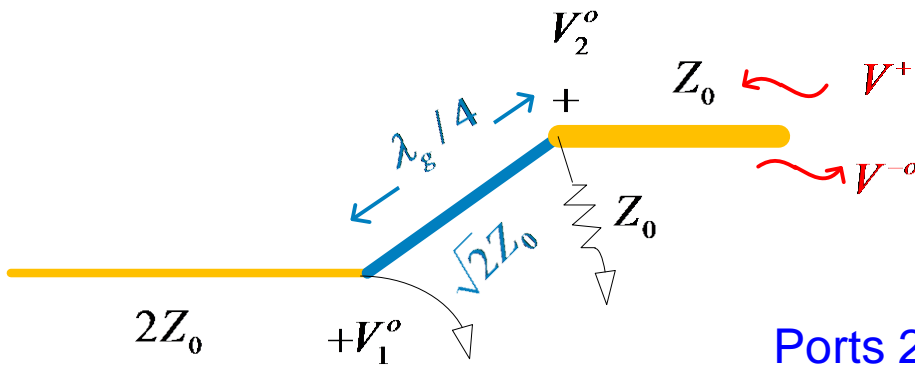
$$S_{32}^e \equiv \frac{V^{-e}}{V^+} = S_{22}^e$$

Note:  $V_3^e = V_2^e$

# Appendix (cont.)

## Port 2 Excitation “odd” problem Overview

**Note:**  
The  $2Z_0$  resistor has been split into two  $Z_0$  resistors in series.

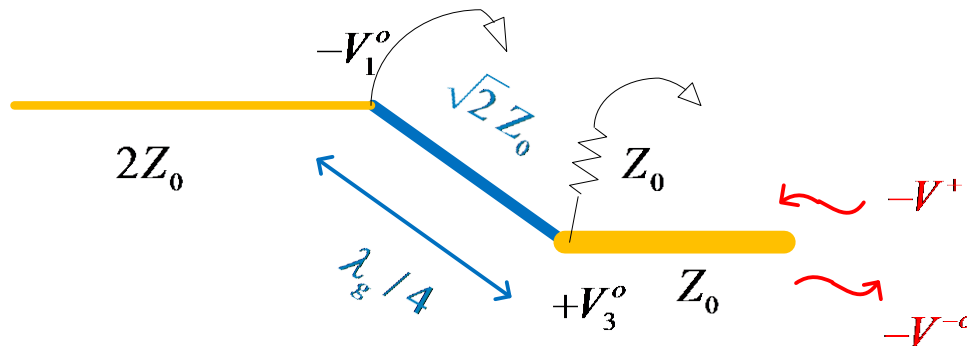


Ports 2 and 3 are excited 180° out of phase.

$$\Rightarrow S_{22}^o, S_{32}^o$$

$$S_{22}^o \equiv \frac{V^{-o}}{V^+}$$

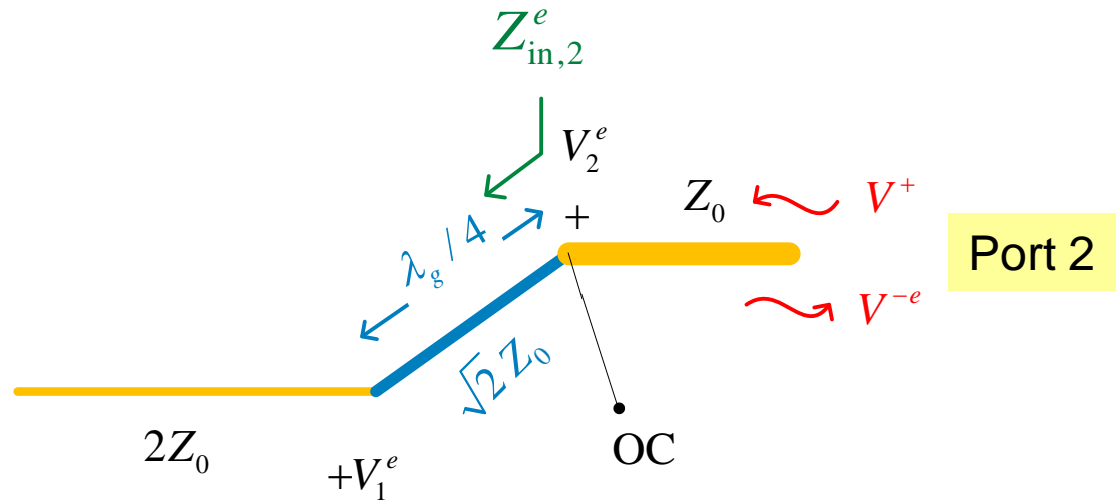
$$S_{32}^o \equiv \frac{-V^{-o}}{V^+} = -S_{22}^o$$



Note:  $V_1^o = 0$ ,  $V_3^o = -V_2^o$

# Appendix (cont.)

Port 2 Excitation  
“even” problem  
Analysis



$$Z_{in2}^e = \frac{(\sqrt{2} Z_0)^2}{2Z_0} = Z_0$$

$$\Rightarrow S_{22}^e = \frac{Z_{in2}^e - Z_0}{Z_{in2}^e + Z_0} = 0$$

Recall:

$$Z_{in} = \frac{Z_T^2}{Z_L}$$

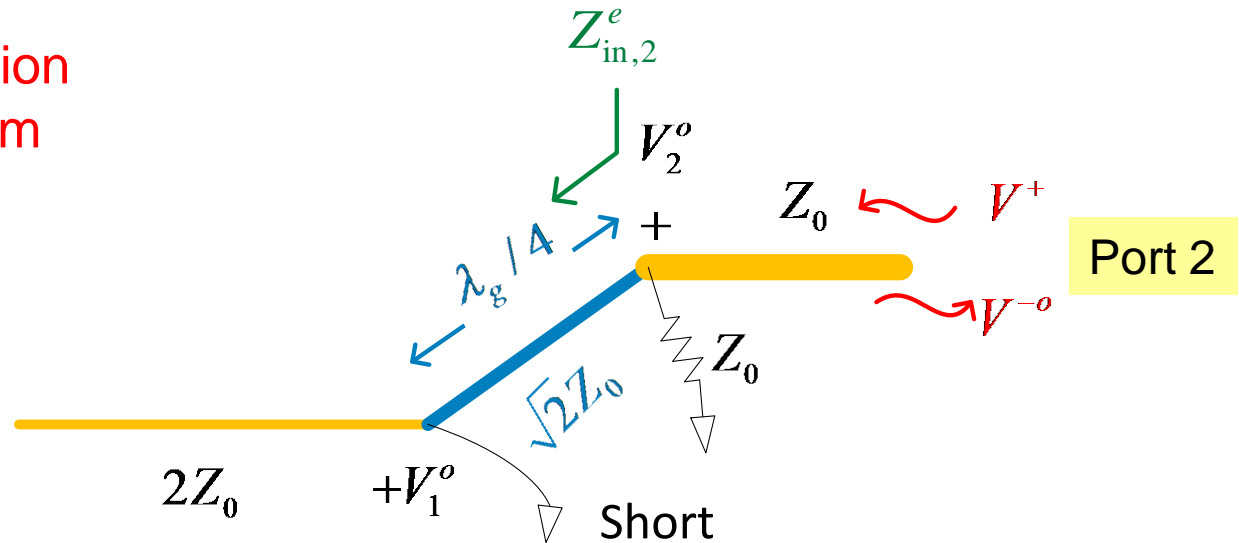
(quarter-wave transformer)

Also, by physical symmetry:  $S_{33}^e = S_{22}^e = 0$

Also, in the even case:  $S_{32}^e = S_{22}^e \Rightarrow S_{32}^e = 0$

# Appendix (cont.)

Port 2 Excitation  
“odd” problem  
Analysis



$$V_1^o = 0$$

$$Z_{in2}^o = \infty \parallel Z_0 = Z_0$$

$$\Rightarrow S_{22}^o = \frac{Z_{in2}^o - Z_0}{Z_{in2}^o + Z_0} = 0$$

Also, by physical symmetry:  $S_{33}^o = S_{22}^o = 0$

Also, in the odd case:  $S_{32}^o = -S_{22}^o \Rightarrow S_{32}^o = 0$

# Appendix (cont.)

We now add the results from the even and odd cases together:

$$S_{22} = \frac{V_2^-}{V_2^+} \Big|_{a_1=a_3=0} = \frac{V^{-e} + V^{-o}}{V^+ + V^+} = \frac{V^{-e} + V^{-o}}{2V^+} = \frac{1}{2} (S_{22}^e + S_{22}^o) = \frac{1}{2} (0 + 0) = 0$$

$\Rightarrow S_{33} = 0$  (by symmetry)

$$S_{32} = \frac{V_3^-}{V_2^+} \Big|_{a_1=a_3=0} = \frac{V^{-e} - V^{-o}}{V^+ + V^+} = \frac{V^{-e} - V^{-o}}{2V^+} = \frac{1}{2} (S_{32}^e - S_{32}^o) = \frac{1}{2} (0 - 0) = 0$$

$\Rightarrow S_{23} = 0$  (by reciprocity)

In summary, for port 2 excitation, we have:

**Note:**  
Since all ports have the same  $Z_0$ , we ignore the normalizing factor  $\sqrt{Z_0}$  in the  $S$  parameter definition.

$$S_{22} = 0$$

$$S_{33} = 0$$

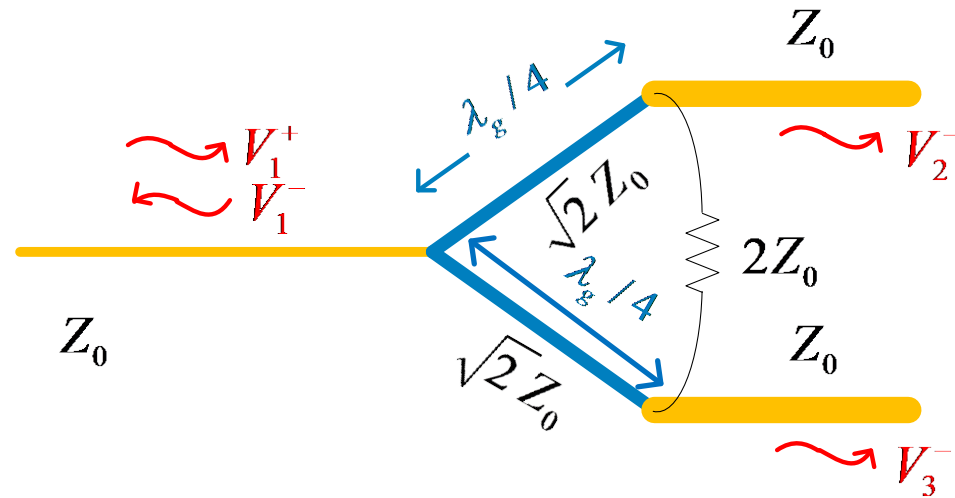
$$S_{32} = S_{23} = 0$$

# Appendix (cont.)

## Port 1 Excitation “even” problem Overview

$$\Rightarrow S_{11}^e, S_{21}^e$$

Port 1



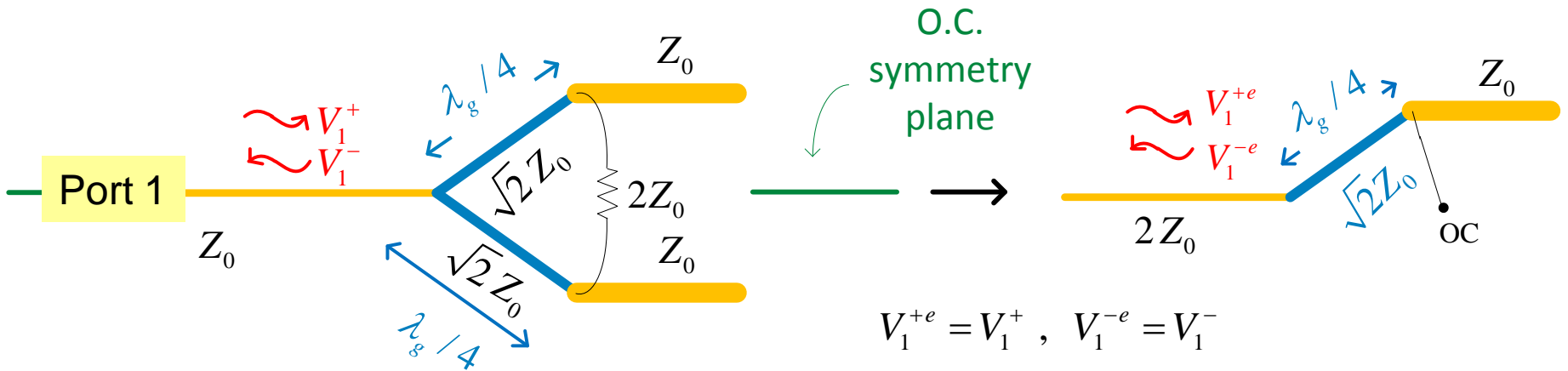
When port 1 is excited, the response, by symmetry, is even.  
(Hence, the total voltages are the same as the even voltages.)



# Appendix (cont.)

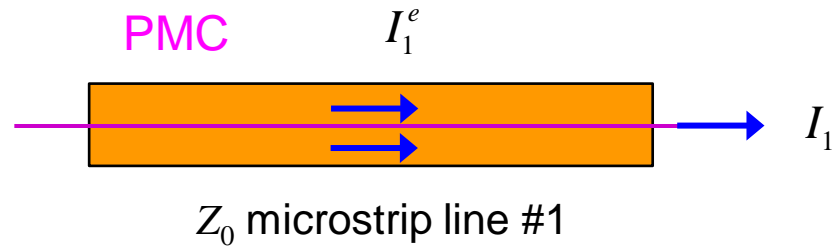
Port 1 Excitation  
 "even" problem  
 Analysis

Top view



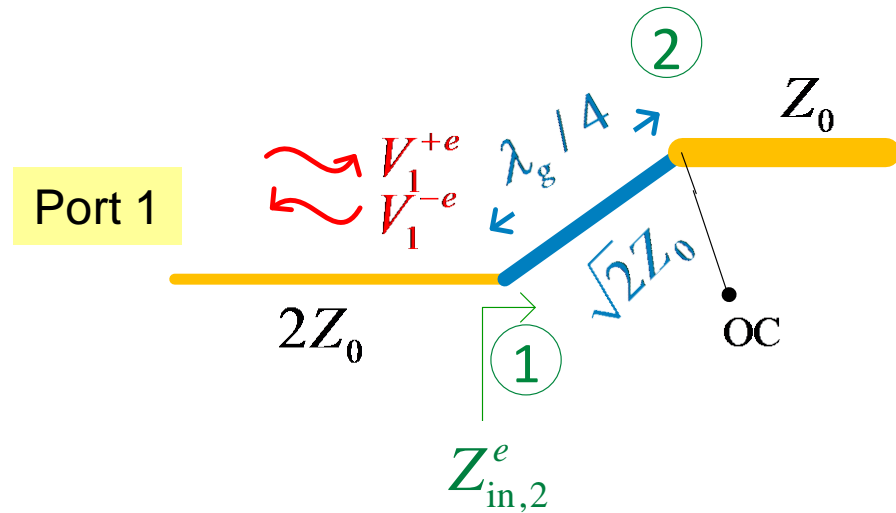
$$I_1^e = I_1 / 2$$

$$V_1^e = V_1$$



# Appendix (cont.)

Port 1 Excitation  
 “even” problem  
 Analysis (cont.)



$$Z_{in1}^e = \frac{(\sqrt{2}Z_0)^2}{Z_0} = 2Z_0$$

$$S_{11}^e = \frac{Z_{in1}^e - 2Z_0}{Z_{in1}^e + 2Z_0} = 0$$

Recall:

$$Z_{in} = \frac{Z_T^2}{Z_L}$$

(quarter-wave transformer)

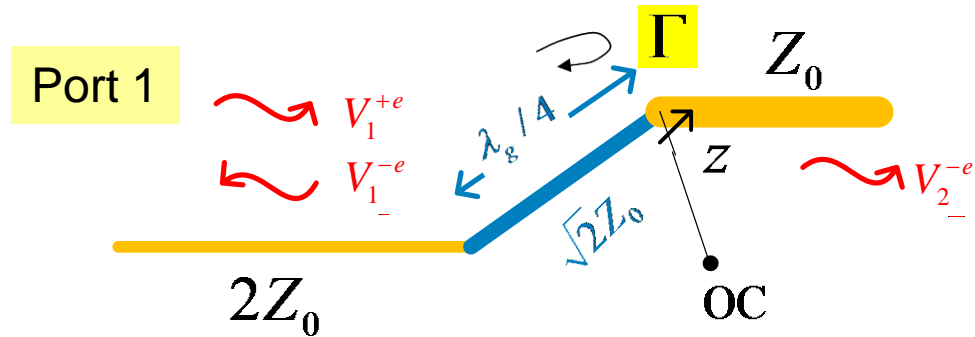
$$S_{11} = \frac{V_1^-}{V_1^+} \Big|_{a_2=a_3=0} = \frac{V_1^{-e}}{V_1^{+e}} \Big|_{a_2=0} = S_{11}^e = 0$$

Hence

$$S_{11} = 0$$

# Appendix (cont.)

Port 1 Excitation  
 "even" problem  
 Analysis (cont.)



Along  $\lambda_g/4$  wave transformer:

$$V_T^e(z) = V_0^+ e^{-j\beta z} (1 + \Gamma e^{+j2\beta z})$$

$z =$  distance from port 2

$$\begin{cases} V_2^e = V_T^e(0) = V_0^+ (1 + \Gamma) \\ V_1^e = V_T^e(-\lambda_g/4) = V_0^+ j(1 - \Gamma) \end{cases}$$

$$\Gamma = \frac{Z_0 - \sqrt{2}Z_0}{Z_0 + \sqrt{2}Z_0} = \frac{1 - \sqrt{2}}{1 + \sqrt{2}}$$

$$1 + \Gamma = \frac{2}{1 + \sqrt{2}} \quad 1 - \Gamma = \frac{2\sqrt{2}}{1 + \sqrt{2}}$$

$$S_{21} = \left. \frac{V_2^-}{V_1^+} \right|_{a_2=a_3=0} = \left. \frac{V_2^{-e}}{V_1^{+e}} \right|_{a_2=a_3=0} = S_{21}^e$$

$$V_2^{-e} = V_2^e$$

$$V_1^e = V_1^{+e} (1 + \cancel{S_{11}^e}) = V_1^{+e}$$

$$\Rightarrow S_{21}^e = \frac{V_2^{-e}}{V_1^{+e}} = \frac{V_2^e}{V_1^e} = -j \frac{(1 + \Gamma)}{(1 - \Gamma)} = -j \frac{2}{2\sqrt{2}}$$

$$\Rightarrow S_{21} = \frac{-j}{\sqrt{2}} = S_{12}$$

(symmetry of S matrix)

# Appendix (cont.)

For the other two components:

By physical symmetry:  $S_{31} = S_{21} = \frac{-j}{\sqrt{2}}$

By reciprocity (symmetry of  $S$  matrix):  $S_{13} = S_{31} = \frac{-j}{\sqrt{2}}$

We then have the final  $S$  matrix:

$$[S] = \frac{-j}{\sqrt{2}} \begin{pmatrix} 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 0 & 0 \end{pmatrix}$$