***Thevenin and Norton Equivalents***

This tutorial is intended to show how Thevenin and Norton Equivalent Circuits can be calculated. In your lecture class, you will cover these topics in more detail, but the information here is enough to perform the ECE 2100 Lab III Exercise.

Before doing Thevenin and Norton Equivalents, we discuss one other important topic: Source Transformations.

***Source Transformations***

**Idea**

With respect to two particular terminals, called “a” and “b” in the circuits below, a voltage source in series with a resistance is equivalent to a current source in parallel with a resistance.

**Discussion**

The circuits “1” and “2” below are ***equivalent with respect to terminals a and b***, provided that *vs, Rs, is,* and *Rp* are related to one another in a particular way. If they are, then a resistor RL connected to terminals a and b will have the same voltage across it (and the same current through it) whether it is connected to circuit 1 or to circuit 2.



Note that it is very important to include the qualifier *with respect to terminals a and b*, because two circuits are not necessarily equivalent at just any two terminals.

**Calculation**

If the parameters are related correctly, a voltage source in series with a resistor can be replaced with a current source in parallel with a resistor. The relationships that must exist between the parameters *v*S, *i*S, RS, and RP are as follows.

A voltage source vS in series with a resistor RS will be equivalent to a current source iS in parallel with a resistor RP if

 and .

This is the ***source transformation theorem***.

***Thevenin and Norton Equivalents***

**Idea**

The behavior of any linear circuit at a specific pair of terminals in a circuit may be modeled by a voltage source vTH in series with a resistor RTH

What we are saying is this:



**Calculation**

The box in the figure below contains an arbitrary linear circuit. We have labeled terminals a) and b). On the right, we have an open circuit at a), b), resulting in an open-circuit voltage *vOC*. (We can think of this as an infinite load resistance.) On the left, we have connected a short to the terminals, resulting in a short-circuit current isc.



By comparing the drawing on the left with the Thevenin Equivalent drawing above, it should be clear that

 .

By comparing the drawing on the left with the Thevenin Equivalent, we can see also that

 .

So if we know the open-circuit voltage and the short-circuit current at the terminals a), b), we can find the Thevenin Equivalent:

 and  .

If this were an experiment, we could measure *vOC* and *iSC*. If it is an analytical problem, we can calculate them using our knowledge of circuit theory.

In Lab III, we will measure and calculate the Thevenin Equivalents for two circuits. We will connect resistors to the terminals of those circuits and measure the current through them, and then we will compare those measurements with calculations predicted by the Thevenin Equivalent. This is illustrated below.

